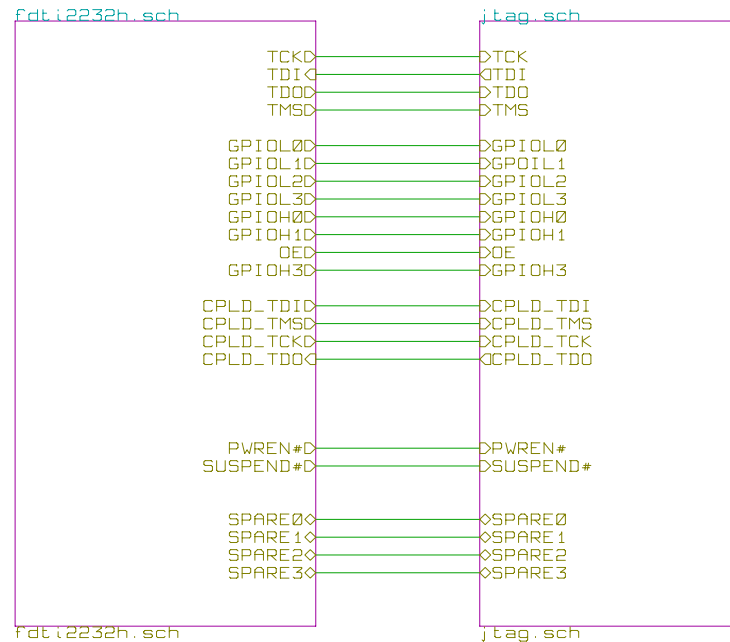
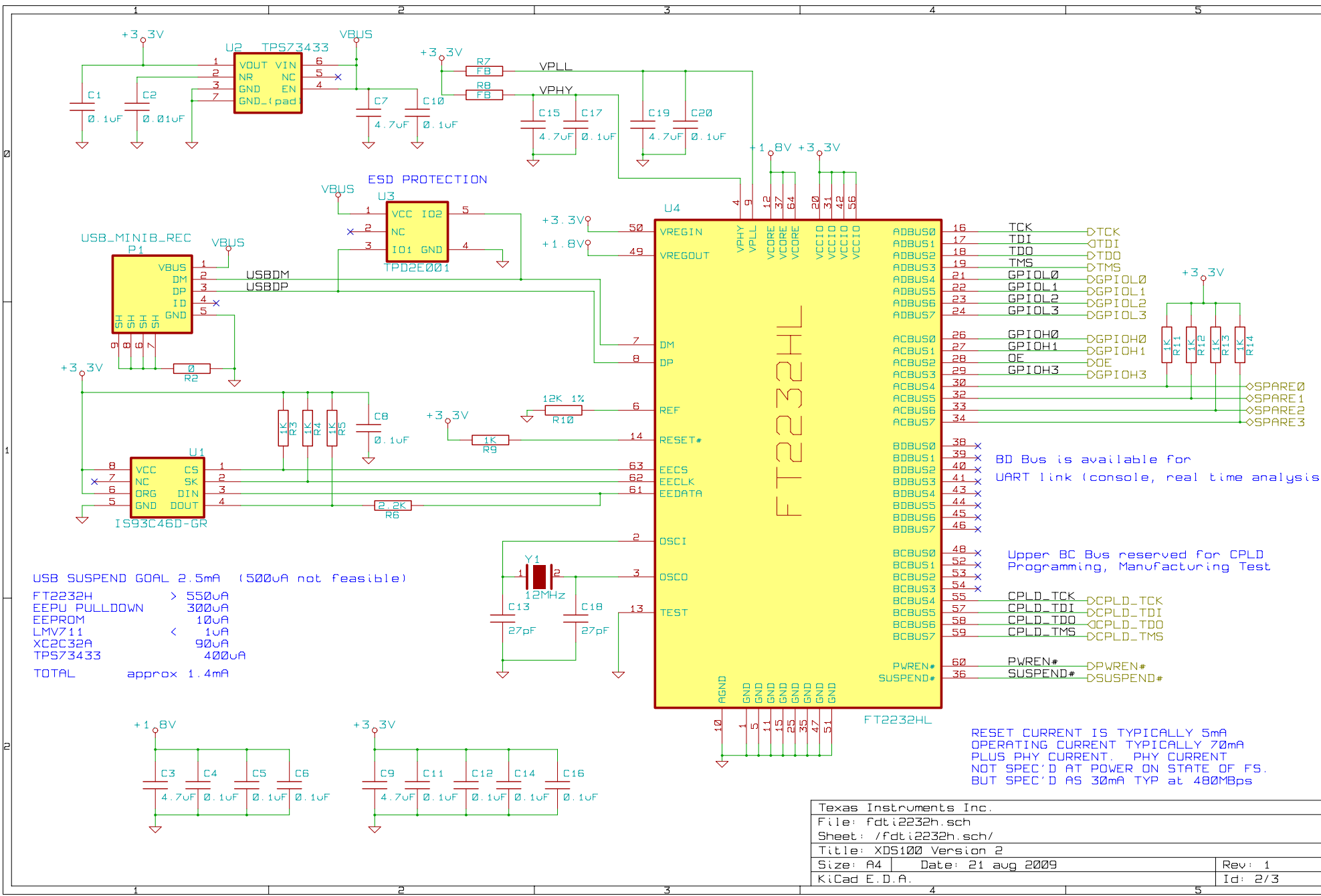


Revision History

- \* Mar 2009: First Six Prototypes
- 1 July 2009
  - Pullup R5 moved to EEPROM Data Out
  - Deleted R1
  - Added Status LED to FTDI 2232H PWREN#
  - Changed opamp to dual version for separation of voltage follower and voltage detection. fixed bug in rev \*
  - Moved CPLD programming pins to upper BC Bus freeing BD Bus for auxiliary UART functions
- 2 August 2009
  - Added 5.1K resistor to opamp U6 pin1 output. Resistor limits over-drive current to ~330uA
- 3 August 2009
  - Fixed embedded BOM Information for TPD4E002
  - Change to USB MiniB PN, (Alignment Post)
  - Fixed footprint error 14 pin JTAG in Layout



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Copyright 2009		
For more information:		
<a href="http://www.tiexpressdsp.com/index.php/XDS100">http://www.tiexpressdsp.com/index.php/XDS100</a>		
File: xds100v2.sch		
Sheet: /		
Title: XDS100 Version 2		
Size: A4	Date: 21 aug 2009	Rev: 1
KiCad E.D.A.		Id: 1/3

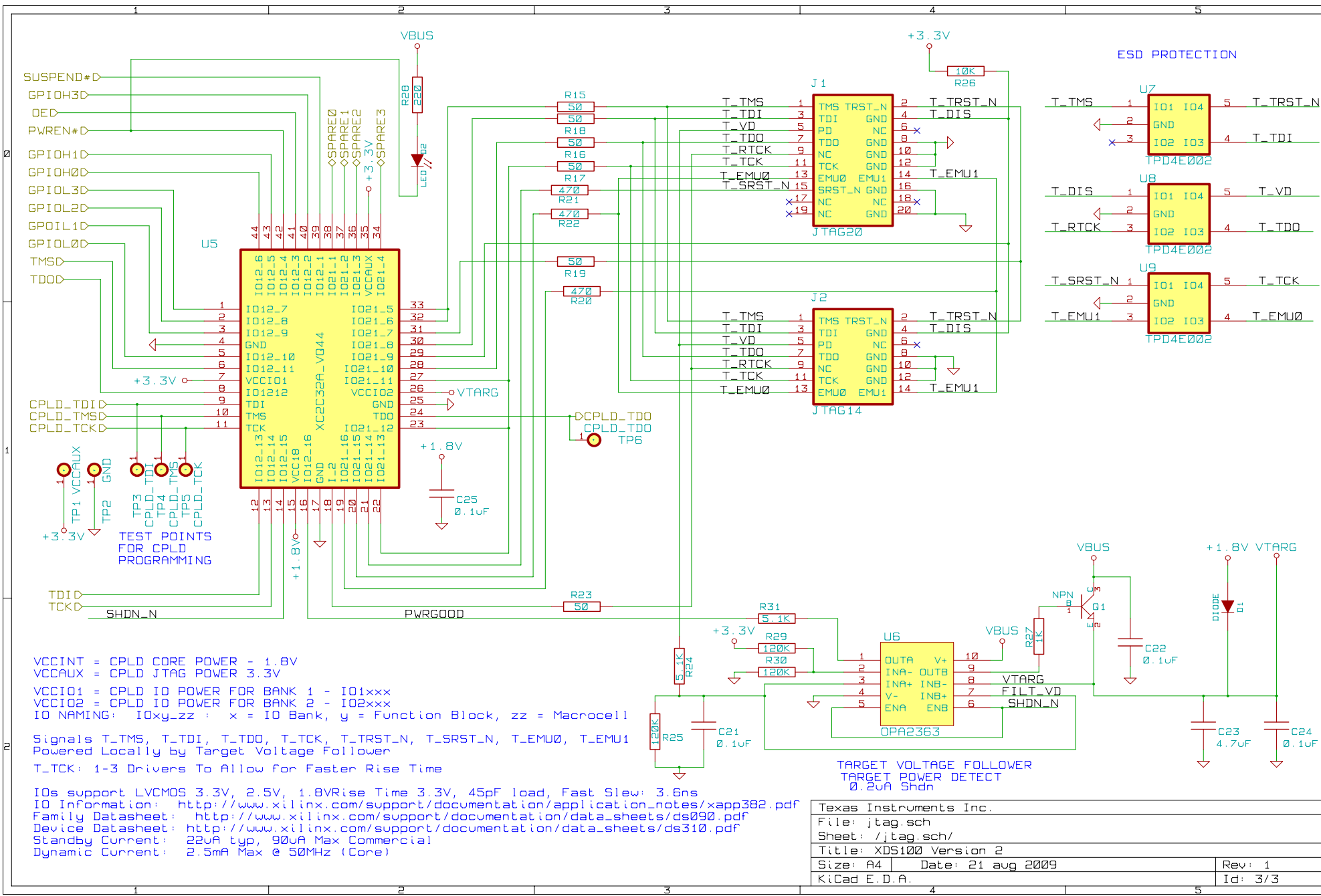


USB SUSPEND GOAL 2.5mA (500uA not feasible)

FT2322H	>	550uA
EEMU PULLDOWN		300uA
EEPROM		10uA
LMV711	<	1uA
XC2C32A		90uA
TPS73433		400uA
TOTAL	approx	1.4mA

RESET CURRENT IS TYPICALLY 5mA  
 OPERATING CURRENT TYPICALLY 70mA  
 PLUS PHY CURRENT. PHY CURRENT  
 NOT SPEC'D AT POWER ON STATE OF FS.  
 BUT SPEC'D AS 30mA TYP at 480Mbps

Texas Instruments Inc.		
File: fdti2232h.sch		
Sheet: /fdti2232h.sch/		
Title: XDS100 Version 2		
Size: A4	Date: 21 aug 2009	Rev: 1
KiCad E.D.A.		Id: 2/3



VCCINT = CPLD CORE POWER - 1.8V  
 VCCAUX = CPLD JTAG POWER 3.3V  
 VCCIO1 = CPLD IO POWER FOR BANK 1 - IO1xxx  
 VCCIO2 = CPLD IO POWER FOR BANK 2 - IO2xxx  
 IO NAMING: IOxy\_zz : x = IO Bank, y = Function Block, zz = Macrocell

Signals T\_TMS, T\_TDI, T\_TDO, T\_TCK, T\_TRST\_N, T\_SRST\_N, T\_EMU0, T\_EMU1  
 Powered Locally by Target Voltage Follower  
 T\_TCK: 1-3 Drivers To Allow for Faster Rise Time

IOs support LVCMOS 3.3V, 2.5V, 1.8V Rise Time 3.3V, 45pF load, Fast Slew: 3.6ns  
 IO Information: [http://www.xilinx.com/support/documentation/application\\_notes/xapp382.pdf](http://www.xilinx.com/support/documentation/application_notes/xapp382.pdf)  
 Family Datasheet: [http://www.xilinx.com/support/documentation/data\\_sheets/ds090.pdf](http://www.xilinx.com/support/documentation/data_sheets/ds090.pdf)  
 Device Datasheet: [http://www.xilinx.com/support/documentation/data\\_sheets/ds310.pdf](http://www.xilinx.com/support/documentation/data_sheets/ds310.pdf)  
 Standby Current: 22uA typ, 90uA Max Commercial  
 Dynamic Current: 2.5mA Max @ 50MHz (Core)

Texas Instruments Inc.		
File: jtag.sch		
Sheet: /jtag.sch/		
Title: XDS100 Version 2		
Size: A4	Date: 21 aug 2009	Rev: 1
KiCad E.D.A.		Id: 3/3