



This circuit is provided provisionally for the purpose of evaluating picture quality and, as such Sony offers no guarantees for this circuit pattern in terms of characteristics or industrial property rights.

"Effio-E" is a trademark of Sony Corporation.

Title: "Effio-E" system reference design circuit / ICX672/673AK + CXD4816GG (Frontend block)			
Sub Title: Effio-E_RD (FE)			
Size A2	Document Number	Rev V1.01	
Date: Wednesday, May 26, 2010	Sheet 1	of 3	

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Title: "Effio-E" system reference design circuit / CXD4127GG (Backend block)			
Sub Title: Effio-E_RD (BE)			
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Date:	Tuesday, May 25, 2010	Sheet 2 of	3

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Title: "Effio-E" system reference design circuit / DCDC converter + LDO (Power supply block)			
Sub Title: Effio-E_RD (Power Supply)			
Size A2	Document Number		Rev V1.01
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