

NTE7090

Variable Output Voltage Regulator (Dual Tracking Type)

Description:

The NTE7090 is a dual tracking, variable output voltage regulator housed in a 8-pin SIP.

The output voltage can be adjusted over a wide range from ± 3 to ± 30 V by adjusting the value of the voltage setting external resistors. By adjusting the resistance of the external balance setting resistors the positive/negative output voltage ratio can also be set freely. Again by attaching power transistors high current gains can be achieved making the device suitable for use in the power supplies of a wide variety of equipment.

Features:

- High Input voltage $V_I = \pm 35$ V
- Wide Range of Output Voltage $V_O = \pm 3$ to ± 30 V
- Low output noise voltage $V_{NO} = 12\mu V_{rms}(typ)$
- Built-in current limiting and thermal shutdown circuit
- The output voltage rise time constant of the coefficients can be adjusted by the value of the external capacitor.
- Capability of operation control by the external control signal (Pin 8).

Application:

Dual voltage power supplies for stereo preamplifiers, for the power supplies of other equipment, including operational amplifiers.

Recommended Operating Conditions:

Supply Voltage Range ± 8 to ± 35 V
Rated Supply Voltage ± 20 V

Absolute Maximum Ratings: ($T_A = +25^\circ\text{C}$ unless otherwise specified)

Input Voltage, V_I ± 35 V
Load Current, I_L ± 30 mA
Input-Output Voltage Difference, $V_I - V_O$ ± 32 V
Power Dissipation, P_D 800mW
Operating Ambient Temperature Range, T_{opr} -20° to $+75^\circ\text{C}$
Storage Temperature Range, T_{stg} -56° to $+125^\circ\text{C}$

Electrical Characteristics: ($T_A = +25^{\circ}\text{C}$, $V_I = \pm 20\text{V}$, $V_O = \pm 15\text{V}$, $I_L = 10\text{mA}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Voltage	V_I		± 8	–	± 35	V
Output Voltage	V_O	$R_2 = 1.5$ to $55\text{k}\Omega$	± 3	–	± 30	V
Reference Voltage	V_{REF}	(Between Pin5 and Pin1)	1.66	1.8	1.95	V
Minimum Input–Output Voltage Difference	$V_I - V_O$		–	2.5	3	V
Dual Voltage Tracking	$\Delta V_{O\pm}$		–	–	1	%
Input Regulation	R_{egin}	$V_I = \pm 18$ to $\pm 30\text{V}$	–	0.02	0.1	%/V
Load Regulation	R_{egL}	$I_L = 0$ to 20mA	–	0.02	0.1	%
Bias Current	I_S	$I_L = 0$	–	1.3	3.0	mA
Temperature Coefficient of Output Voltage	TC_{V_O}	$T_A = 0^{\circ}$ to 75°C , $V_O = \pm 3$ to $\pm 30\text{V}$	–	0.01	–	%/ $^{\circ}\text{C}$
Ripple Rejection	RR	$f = 120\text{Hz}$	–	68	–	dB
Output Noise Voltage	V_{HO}	$f = 20\text{Hz}$ to 100kHz (between the output terminal and ground)	–	12	–	μV_{rms}
Output Cut–Off Voltage	$V_{O(\text{off})}$	$V_I = 10\text{V}$	–	–	± 0.1	V

Pin Connection Diagram
(Front View)

