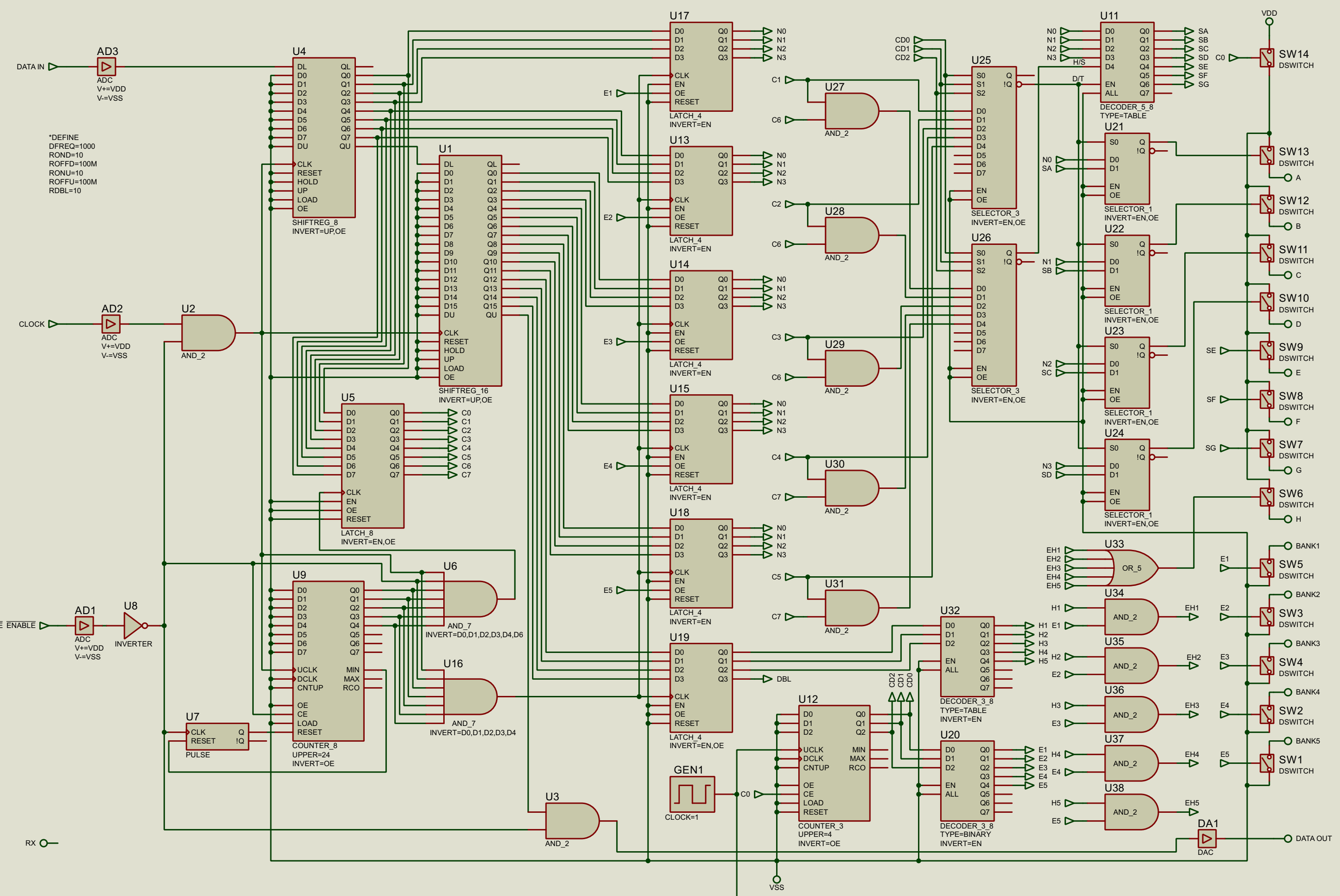
DATA IN
AD3
ADC
V+=VDD
V-=VSS
U4
SHIFTREG_8
INVERT=UP,OE
*DEFINE
DFREQ=1000
ROND=10
ROFFD=100M
RONU=10
ROFFU=100M
RDBL=10
U1
SHIFTREG_16
INVERT=UP,OE
CLOCK
AD2
ADC
V+=VDD
V-=VSS
U2
AND_2
U5
LATCH_8
INVERT=EN,OE
U9
COUNTER_8
UPPER=24
INVERT=OE
U6
AND_7
INVERT=D0,D1,D2,D3,D4,D6
U16
AND_7
INVERT=D0,D1,D2,D3,D4
U7
PULSE
ENABLE
AD1
ADC
V+=VDD
V-=VSS
U8
INVERTER
RX
U3
AND_2
U17
LATCH_4
INVERT=EN
U13
LATCH_4
INVERT=EN
U14
LATCH_4
INVERT=EN
U15
LATCH_4
INVERT=EN
U18
LATCH_4
INVERT=EN
U19
LATCH_4
INVERT=EN,OE
DBL
GEN1
CLOCK=1
U27
AND_2
U28
AND_2
U29
AND_2
U30
AND_2
U31
AND_2
U12
COUNTER_3
UPPER=4
INVERT=OE
VSS
U25
SELECTOR_3
INVERT=EN,OE
U26
SELECTOR_3
INVERT=EN,OE
U32
DECODER_3_8
TYPE=TABLE
INVERT=EN
U20
DECODER_3_8
TYPE=BINARY
INVERT=EN
U11
DECODER_5_8
TYPE=TABLE
H/S
D/T
U21
SELECTOR_1
INVERT=EN,OE
U22
SELECTOR_1
INVERT=EN,OE
U23
SELECTOR_1
INVERT=EN,OE
U24
SELECTOR_1
INVERT=EN,OE
U33
OR_5
U34
AND_2
U35
AND_2
U36
AND_2
U37
AND_2
U38
AND_2
VDD
SW14
DSWITCH
SW13
DSWITCH
SW12
DSWITCH
SW11
DSWITCH
SW10
DSWITCH
SW9
DSWITCH
SW8
DSWITCH
SW7
DSWITCH
SW6
DSWITCH
SW5
DSWITCH
SW3
DSWITCH
SW4
DSWITCH
SW2
DSWITCH
SW1
DSWITCH
BANK1
BANK2
BANK3
BANK4
BANK5
DA1
DAC
DATA OUT