

LM4917 Boomer® Audio Power Amplifier Series Ground-Referenced, 95mW Stereo Headphone Amplifier

Check for Samples: [LM4917](#)

FEATURES

- Ground referenced outputs
- High PSRR
- Available in space-saving TSSOP package
- Ultra low current shutdown mode
- Improved pop and click circuitry eliminates noises during turn-on and turn-off transitions
- 1.4 – 3.6V operation
- No output coupling capacitors, snubber networks, bootstrap capacitors

- Shutdown either channel independently

APPLICATIONS

- Notebook PCs
- Desktop PCs
- Mobile Phone
- PDAs
- Portable electronic devices

DESCRIPTION

The LM4917 is a stereo, output capacitor-less headphone amplifier capable of delivering 95mW of continuous average power into a 16Ω load with less than 1% THD+N from a single 3V power supply.

The LM4917 provides high quality audio reproduction with minimal external components. A ground referenced output eliminates the output coupling capacitors typically required by single-ended loads, reducing component count, cost and board space consumption. This makes the LM4917 ideal for mobile phones and other portable equipment where board space is at a premium. Eliminating the output coupling capacitors also improves low frequency response.

The LM4917 operates from a single 1.4V to 3.6V power supply, features low 0.02% THD+N and 70dB PSRR. Independent right/left channel low-power shutdown controls provide power saving flexibility for mono/stereo applications. Superior click and pop suppression eliminates audible transients during start up and shutdown. Short circuit and thermal overload protection protects the device during fault conditions.

Table 1. Key Specifications

	VALUE	UNIT
■ Improved PSRR at 1kHz	70	dB (typ)
■ Power Output at $V_{DD} = 3V$, $R_L = 16\Omega$, $THD \leq 1\%$	95	mW (typ)
■ Shutdown Current	0.01	μA (typ)



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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Block Diagram

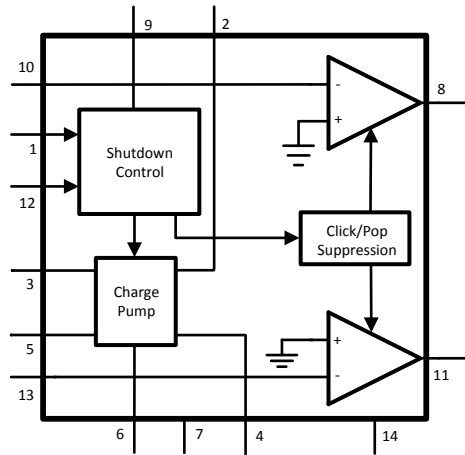


Figure 1. Circuit Block Diagram

Typical Application

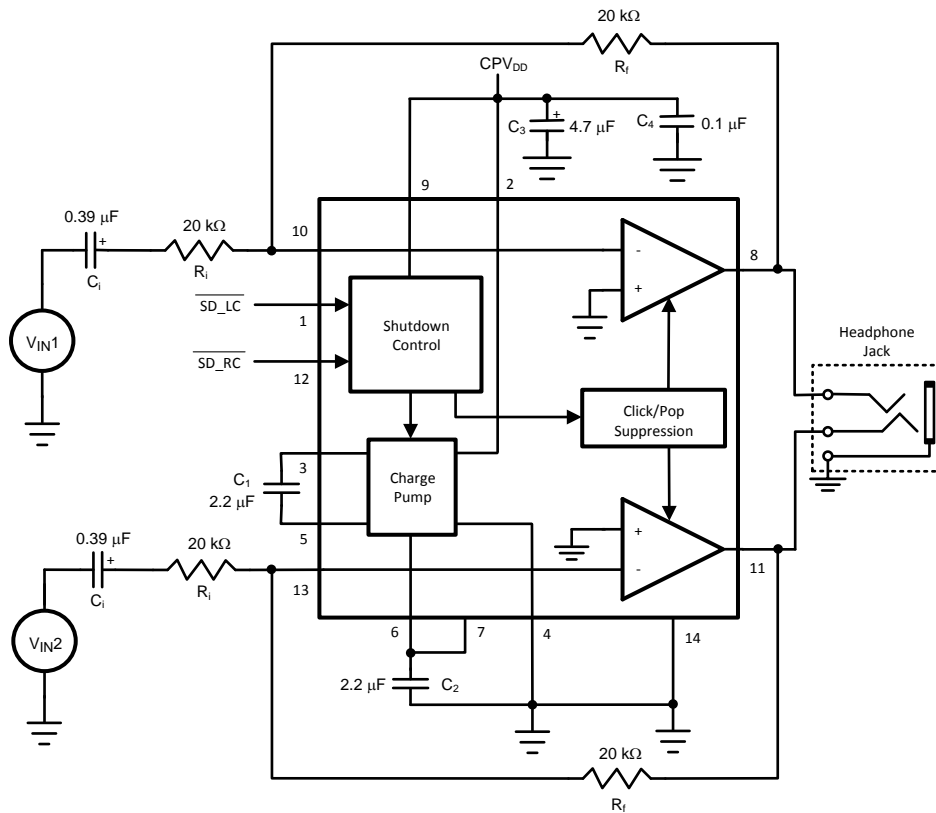
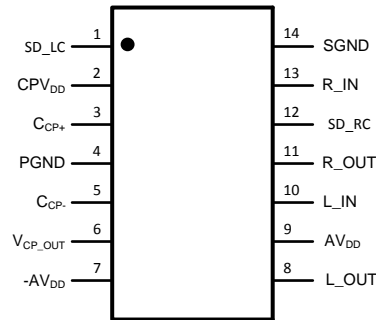
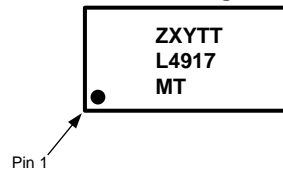


Figure 2. Typical Audio Amplifier Application Circuit

**TSSOP Package
Top View**

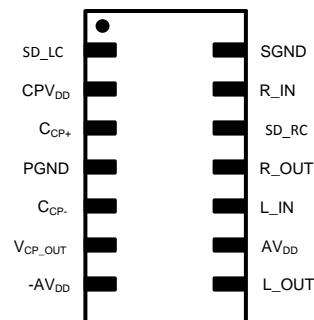


TSSOP Marking

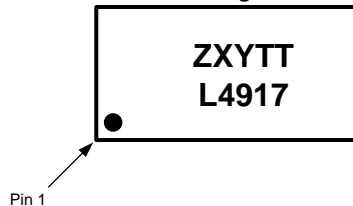


Z - Assembly Plant Code, XY - Date Code, TT - Traceability

**LLP Package
Top View**



LLP Marking



A. Z - Assembly Plant Code, XY - Date Code, TT - Traceability

Pin Descriptions

Pin	Name	Function
1	SD_LC	Active_Low Shutdown, Left Channel
2	CPVDD	Charge Pump Power Supply
3	CCP+	Positive Terminal-Charge Pump Flying Capacitor
4	PGND	Power Ground

Pin Descriptions (continued)

Pin	Name	Function
5	C _{CP-}	Negative Terminal- Charge Pump Flying Capacitor
6	V _{CP_OUT}	Charge Pump Output
7	-AV _{DD}	Negative Power Supply-Amplifier
8	L_OUT	Left Channel Output
9	AV _{DD}	Positive Power Supply-Amplifier
10	L_IN	Left Channel Input
11	R_OUT	Right Channel Output
12	SD_RC	Active_Low Shutdown, Right Channel
13	R_IN	Right Channel Input
14	SGND	Signal Ground

Absolute Maximum Ratings ⁽¹⁾

Supply Voltage	4.0V
Storage Temperature	-65°C to +150°C
Input Voltage	-0.3V to V _{DD} + 0.3V
Power Dissipation ⁽²⁾	Internally Limited
ESD Susceptibility ⁽³⁾	2000V
ESD Susceptibility ⁽⁴⁾	200V
Junction Temperature	150°C
Thermal Resistance	
θ _{JC} (TSSOP)	40°C/W
θ _{JA} (TSSOP)	109°C/W

- (1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional but do not specify performance limits. Electrical Characteristics state DC and AC electrical specifications under particular test conditions that guarantee specific performance limits. This assumes that the device is within the Operating Ratings. Specifications are not guaranteed for parameters where no limit is given; however, the typical value is a good indication of device performance.
- (2) The maximum power dissipation must be derated at elevated temperatures and is dictated by T_{JMAX}, θ_{JA}, and the ambient temperature, T_A. The maximum allowable power dissipation is P_{DMAX} = (T_{JMAX} - T_A) / θ_{JA} or the number given in Absolute Maximum Ratings, whichever is lower. For the LM4917, see power de-rating currents for more information.
- (3) Human body model, 100pF discharged through a 1.5kΩ resistor.
- (4) Machine Model, 220pF-240pF discharged through all pins.

Operating Ratings

Temperature Range	
T _{MIN} ≤ T _A ≤ T _{MAX}	-40°C ≤ T _A ≤ 85°C
Supply Voltage (V _{DD})	1.4V ≤ V _{CC} ≤ 3.6V

Electrical Characteristics $V_{DD} = 3V$ ⁽¹⁾

The following specifications apply for $V_{DD} = 3V$, $A_V = 1$, and 16Ω load unless otherwise specified. Limits apply to $T_A = 25^\circ C$.

Symbol	Parameter	Conditions	LM4917		Units (Limits)
			Typ ⁽²⁾	Limit ^{(3) (4)}	
I_{DD}	Quiescent Power Supply Current	$V_{IN} = 0V$, $I_O = 0A$, both channels enabled	11	20	mA (max)
		$V_{IN} = 0V$, $I_O = 0A$, one channel enabled	9		mA
I_{SD}	Shutdown Current	$V_{SD_LC} = V_{SD_RC} = GND$	0.01	1	μA (max)
V_{OS}	Output Offset Voltage	$R_L = 32\Omega$	1	10	mV (max)
P_O	Output Power	THD+N = 1% (max); $f = 1kHz$, $R_L = 16\Omega$	95	50	mW (min)
		THD+N = 1% (max); $f = 1kHz$, $R_L = 32\Omega$	82		mW
THD+N	Total Harmonic Distortion + Noise	$P_O = 50mW$, $f = 1kHz$, $R_L = 32\Omega$ (A-weighted) single channel	0.02		%
PSRR	Power Supply Rejection Ratio	$V_{RIPPLE} = 200mV$ sine p-p, $f = 1kHz$	70		dB
		$f = 20kHz$	55		
SNR	Signal-to-Noise Ratio	$R_L = 32\Omega$, $P_{OUT} = 20mW$, $f = 1kHz$	100		dB
V_{IH}	Shutdown Input Voltage High		$V_{IH} = 0.7 \cdot CPV_{DD}$		V (min)
V_{IL}	Shutdown Input Voltage Low		$V_{IL} = 0.3 \cdot CPV_{DD}$		V (max)
T_{WU}	Wake Up Time From Shutdown		339		μs (max)
X_{TALK}	Crosstalk	$R_L = 16\Omega$, $P_O = 1.6mW$, $f = 1kHz$	70		dB
I_L	Input Leakage Current		± 0.1		nA

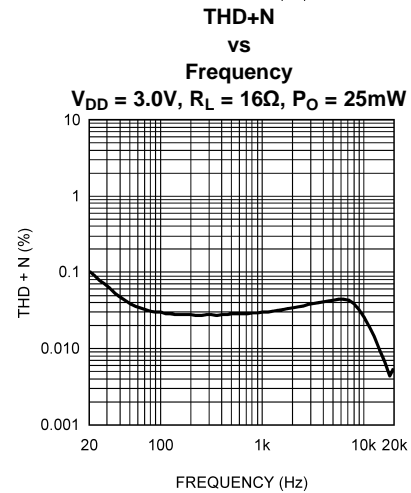
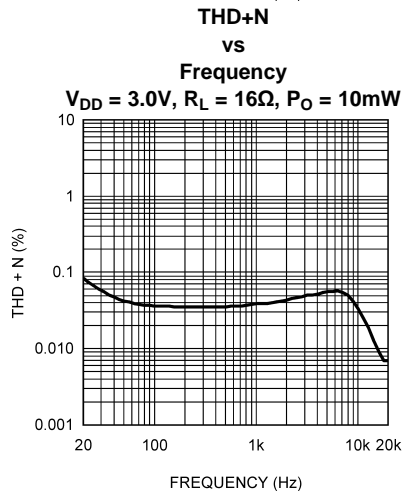
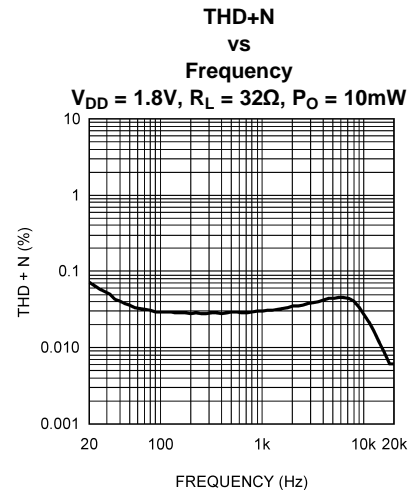
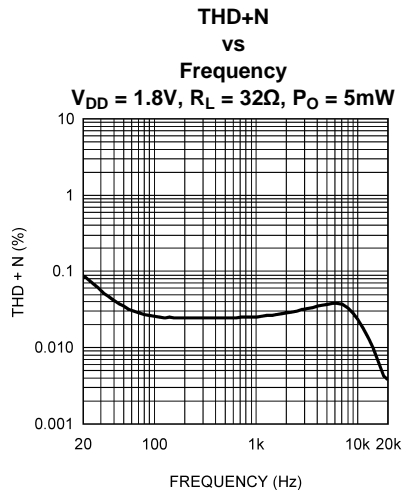
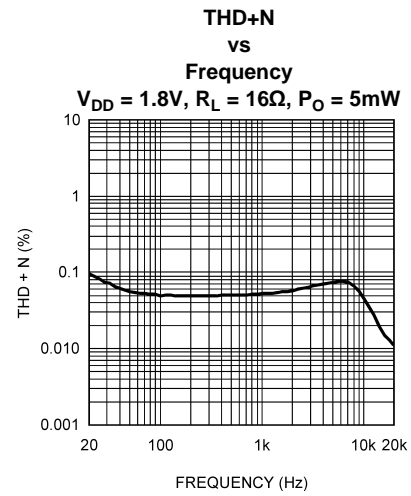
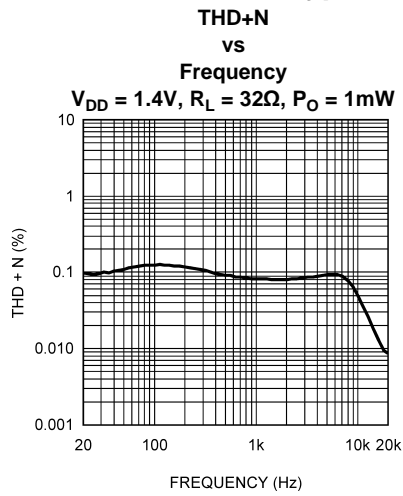
- (1) All voltages are measured with respect to the GND pin unless otherwise specified.
- (2) Typicals are measured at $25^\circ C$ and represent the parametric norm.
- (3) Limits are specified to AOQL (Average Outgoing Quality Level).
- (4) Datasheet min/max specification limits are specified by design, test, or statistical analysis.

External Components Description

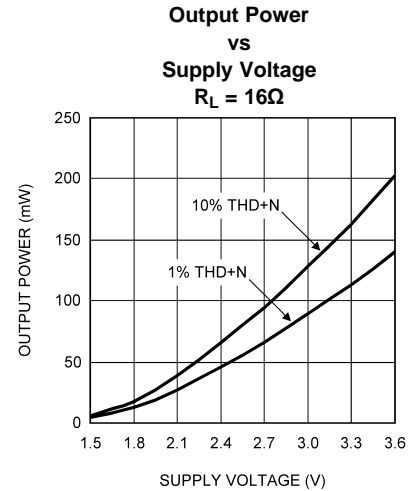
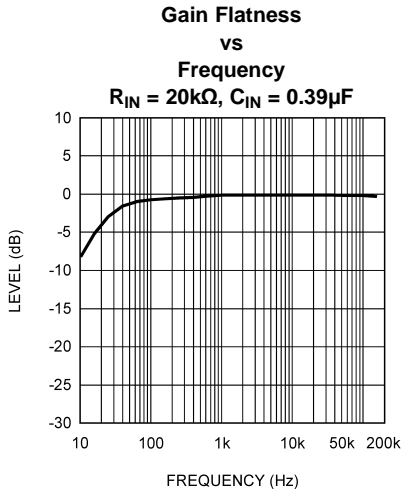
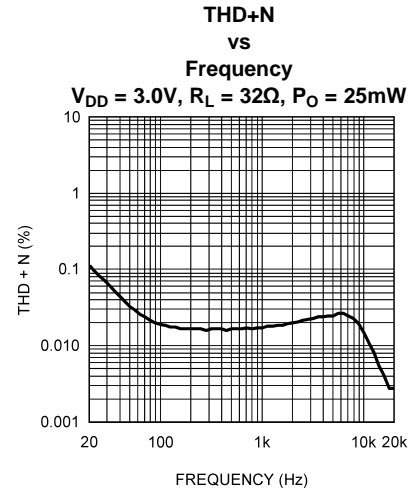
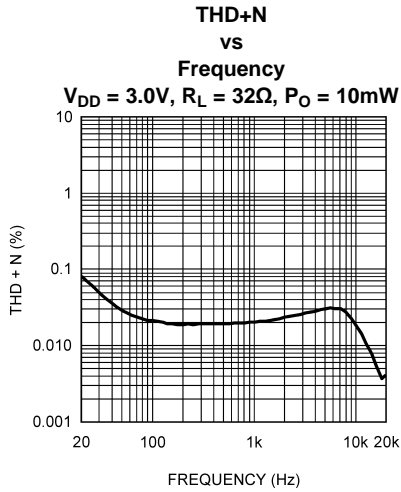
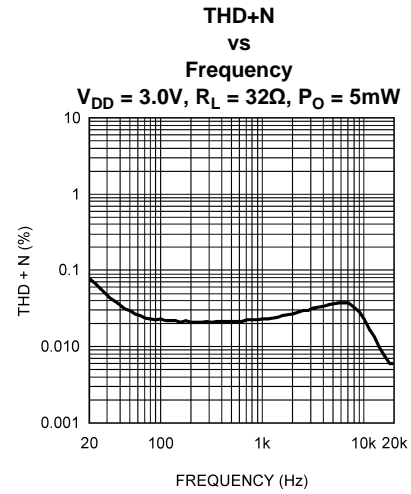
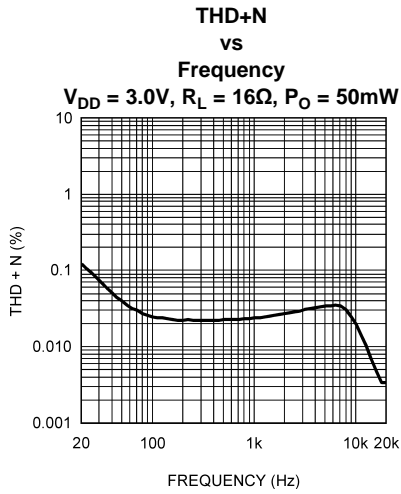
(Figure 1)

Components		Functional Description
1.	R_i	Inverting input resistance which sets the closed-loop gain in conjunction with R_f . This resistor also forms a high-pass filter with C_i at $f_c = 1 / (2\pi R_i C_i)$.
2.	C_i	Input coupling capacitor which blocks the DC voltage at the amplifier's input terminals. Also creates a high-pass filter with R_i at $f_c = 1 / (2\pi R_i C_i)$. Refer to the section Proper Selection of External Components , for an explanation of how to determine the value of C_i .
3.	R_f	Feedback resistance which sets the closed-loop gain in conjunction with R_i .
4.	C_1	Flying capacitor. Low ESR ceramic capacitor ($\leq 100m\Omega$)
5.	C_2	Output capacitor. Low ESR ceramic capacitor ($\leq 100m\Omega$)
6.	C_3	Tantalum capacitor. Supply bypass capacitor which provides power supply filtering. Refer to the Power Supply Bypassing section for information concerning proper placement and selection of the supply bypass capacitor.
7.	C_4	Ceramic capacitor. Supply bypass capacitor which provides power supply filtering. Refer to the Power Supply Bypassing section for information concerning proper placement and selection of the supply bypass capacitor.

Typical Performance Characteristics

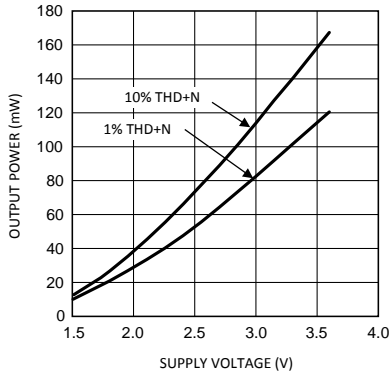


Typical Performance Characteristics (continued)

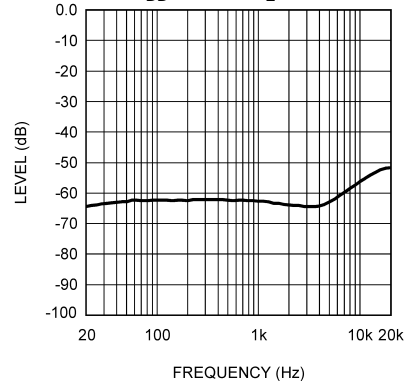


Typical Performance Characteristics (continued)

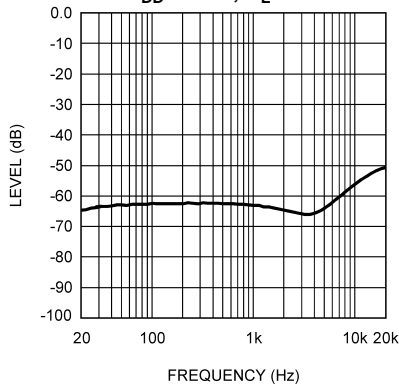
Output Power vs Supply Voltage $R_L = 32\Omega$



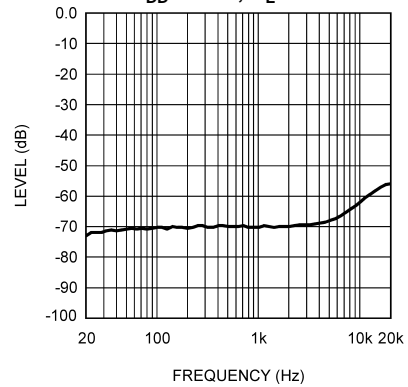
PSRR vs Frequency $V_{DD} = 1.8V, R_L = 16\Omega$



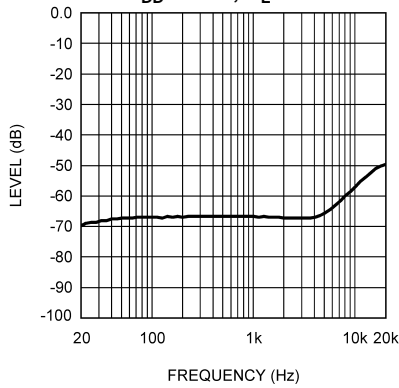
PSRR vs Frequency $V_{DD} = 1.8V, R_L = 32\Omega$



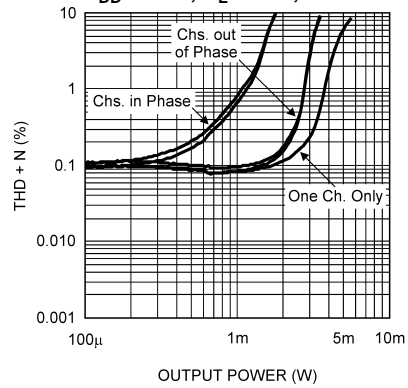
PSRR vs Frequency $V_{DD} = 3.0V, R_L = 16\Omega$



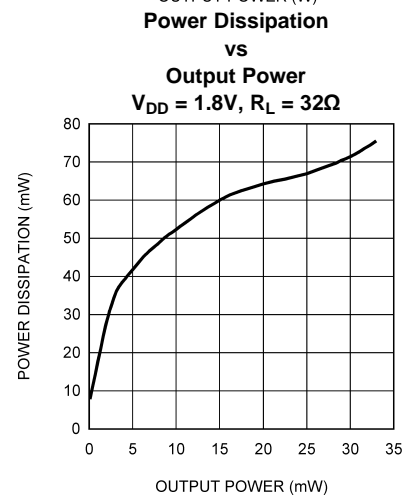
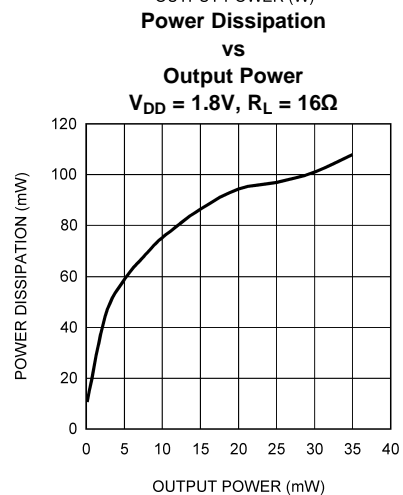
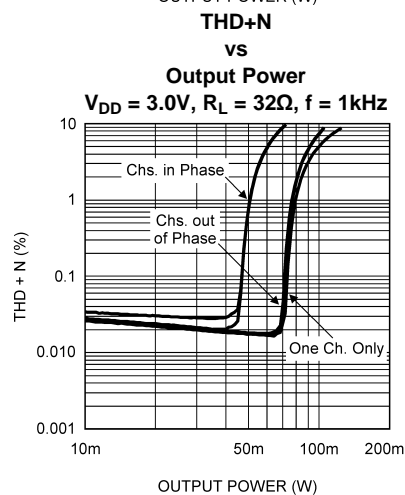
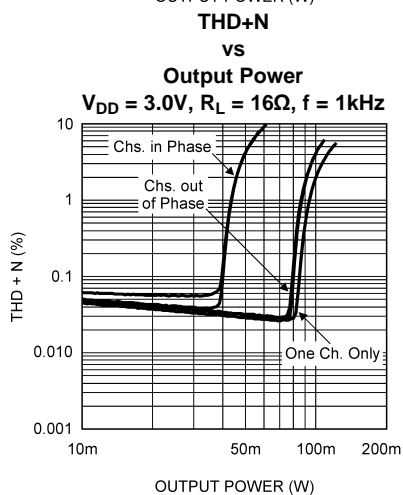
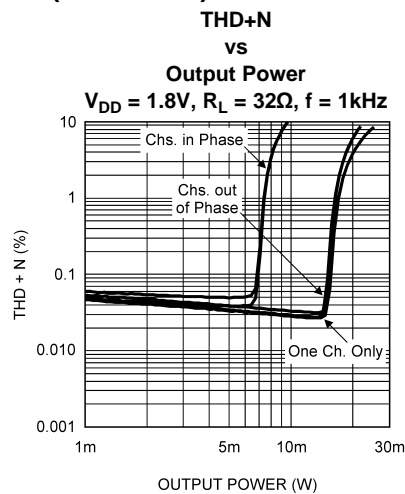
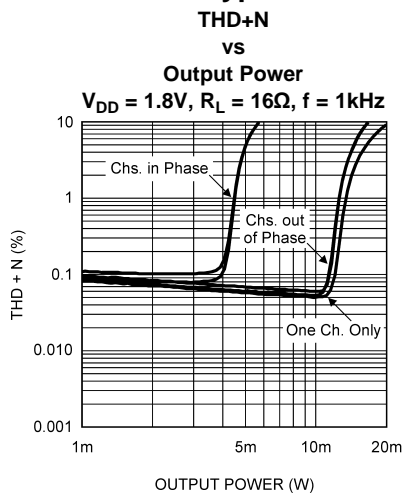
PSRR vs Frequency $V_{DD} = 3.0V, R_L = 32\Omega$



THD+N vs Output Power $V_{DD} = 1.4V, R_L = 32\Omega, f = 1kHz$

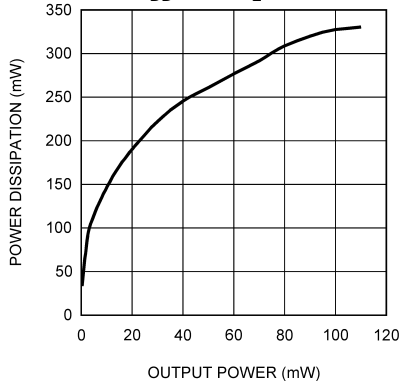


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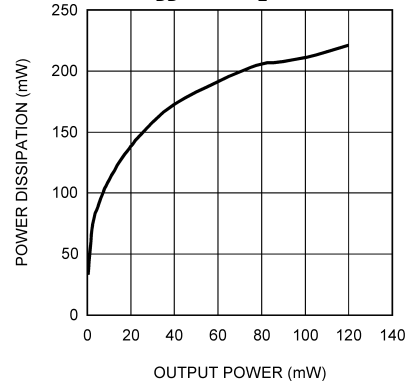


Typical Performance Characteristics (continued)

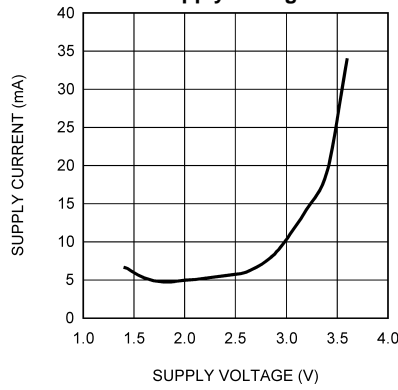
Power Dissipation vs Output Power
 $V_{DD} = 3V, R_L = 16\Omega$



Power Dissipation vs Output Power
 $V_{DD} = 3V, R_L = 32\Omega$



Supply Current vs Supply Voltage



APPLICATION INFORMATION

ELIMINATING THE OUTPUT COUPLING CAPACITOR

The LM4917 features a low noise inverting charge pump that generates an internal negative supply voltage. This allows the outputs of the LM4917 to be biased about GND instead of a nominal DC voltage, like traditional headphone amplifiers. Because there is no DC component, the large DC blocking capacitors (typically 220µF) are not necessary. The coupling capacitors are replaced by two, small ceramic charge pump capacitors, saving board space and cost.

Eliminating the output coupling capacitors also improves low frequency response. The headphone impedance and the output capacitor form a high pass filter that not only blocks the DC component of the output, but also attenuates low frequencies, impacting the bass response. Because the LM4917 does not require the output coupling capacitors, the low frequency response of the device is not degraded by external components.

In addition to eliminating the output coupling capacitors, the ground referenced output nearly doubles the available dynamic range of the LM4917 when compared to a traditional headphone amplifier operating from the same supply voltage.

OUTPUT TRANSIENT ('CLICK AND POPS') ELIMINATED

The LM4917 contains advanced circuitry that virtually eliminates output transients ('clicks and pops'). This circuitry prevents all traces of transients when the supply voltage is first applied or when the part resumes operation after coming out of shutdown mode.

To ensure optimal click and pop performance under low gain configurations (less than 0dB), it is critical to minimize the RC combination of the feedback resistor R_F and stray input capacitance at the amplifier inputs. A more reliable way to lower gain or reduce power delivered to the load is to place a current limiting resistor in series with the load as explained in the **Minimizing Output Noise / Reducing Output Power** section.

AMPLIFIER CONFIGURATION EXPLANATION

As shown in [Figure 2](#), the LM4917 has two operational amplifiers internally. The two amplifiers have externally configurable gain, and the closed loop gain is set by selecting the ratio of R_f to R_i . Consequently, the gain for each channel of the IC is

$$A_V = -(R_f / R_i) \quad (1)$$

Since this an output ground-referenced amplifier, by driving the headphone through R_{OUT} (Pin 11) and L_{OUT} (Pin 8), the LM4917 does not require output coupling capacitors. The typical single-ended amplifier configuration where one side of the load is connected to ground requires large, expensive output capacitors.

POWER DISSIPATION

Power dissipation is a major concern when using any power amplifier and must be thoroughly understood to ensure a successful design. [Equation 1](#) states the maximum power dissipation point for a single-ended amplifier operating at a given supply voltage and driving a specified output load.

$$P_{DMAX} = (V_{DD})^2 / (2\pi^2 R_L) \quad (2)$$

Since the LM4917 has two operational amplifiers in one package, the maximum internal power dissipation point is twice that of the number which results from [Equation 1](#). Even with the large internal power dissipation, the LM4917 does not require heat sinking over a large range of ambient temperature. From [Equation 1](#), assuming a 3V power supply and a 16Ω load, the maximum power dissipation point is 28mW per amplifier. Thus the maximum package dissipation point is 56mW. The maximum power dissipation point obtained must not be greater than the power dissipation that results from [Equation 2](#):

$$P_{DMAX} = (T_{JMAX} - T_A) / (\theta_{JA}) \quad (3)$$

For package TSSOP, $\theta_{JA} = 109^{\circ}\text{C/W}$. $T_{JMAX} = 150^{\circ}\text{C}$ for the LM4917. Depending on the ambient temperature, T_A , of the system surroundings, Equation 2 can be used to find the maximum internal power dissipation supported by the IC packaging. If the result of Equation 1 is greater than that of Equation 2, then either the supply voltage must be decreased, the load impedance increased or T_A reduced. For the typical application of a 3V power supply, with a 16Ω load, the maximum ambient temperature possible without violating the maximum junction temperature is approximately 119.9°C provided that device operation is around the maximum power dissipation point. Power dissipation is a function of output power and thus, if typical operation is not around the maximum power dissipation point, the ambient temperature may be increased accordingly. Refer to the **Typical Performance Characteristics** curves for power dissipation information for lower output powers.

POWER SUPPLY BYPASSING

As with any power amplifier, proper supply bypassing is critical for low noise performance and high power supply rejection. Applications that employ a 3V power supply typically use a $4.7\mu\text{F}$ in parallel with a $0.1\mu\text{F}$ ceramic filter capacitors to stabilize the power supply's output, reduce noise on the supply line, and improve the supply's transient response. However, their presence does not eliminate the need for a local $0.1\mu\text{F}$ supply bypass capacitor, C_S , connected between the LM4917's supply pins and ground. Keep the length of leads and traces that connect capacitors between the LM4917's power supply pin and ground as short as possible.

MICRO POWER SHUTDOWN

The voltage applied to the $\overline{\text{SD_LC}}$ (shutdown left channel) pin and the $\overline{\text{SD_RC}}$ (shutdown right channel) pin controls the LM4917's shutdown function. When active, the LM4917's micropower shutdown feature turns off the amplifiers' bias circuitry, reducing the supply current. The trigger point is $0.3 \times \text{CPV}_{DD}$ for a logic-low level, and $0.7 \times \text{CPV}_{DD}$ for logic-high level. The low $0.01\mu\text{A}(\text{typ})$ shutdown current is achieved by applying a voltage that is as near as ground as possible to the $\overline{\text{SD_LC}}/\overline{\text{SD_RC}}$ pins. A voltage that is higher than ground may increase the shutdown current.

There are a few ways to control the micro-power shutdown. These include using a single-pole, single-throw switch, a microprocessor, or a microcontroller. When using a switch, connect an external $100\text{k}\Omega$ pull-up resistor between the $\overline{\text{SD_LC}}/\overline{\text{SD_RC}}$ pins and V_{DD} . Connect the switch between the $\overline{\text{SD_LC}}/\overline{\text{SD_RC}}$ pins and ground. Select normal amplifier operation by opening the switch. Closing the switch connects the $\overline{\text{SD_LC}}/\overline{\text{SD_RC}}$ pins to ground, activating micro-power shutdown. The switch and resistor guarantee that the $\overline{\text{SD_LC}}/\overline{\text{SD_RC}}$ pins will not float. This prevents unwanted state changes. In a system with a microprocessor or microcontroller, use a digital output to apply the control voltage to the $\overline{\text{SD_LC}}/\overline{\text{SD_RC}}$ pins. Driving the $\overline{\text{SD_LC}}/\overline{\text{SD_RC}}$ pins with active circuitry eliminates the pull-up resistor.

SELECTING PROPER EXTERNAL COMPONENTS

Optimizing the LM4917's performance requires properly selecting external components. Though the LM4917 operates well when using external components with wide tolerances, best performance is achieved by optimizing component values.

The LM4917 is unity-gain stable, giving a designer maximum design flexibility. The gain should be set to no more than a given application requires. This allows the amplifier to achieve minimum THD+N and maximum signal-to-noise ratio. These parameters are compromised as the closed-loop gain increases. However, low gain demands input signals with greater voltage swings to achieve maximum output power. Fortunately, many signal sources such as audio CODECs have outputs of $1V_{RMS}$ ($2.83V_{P-P}$). Please refer to the **Audio Power Amplifier Design** section for more information on selecting the proper gain.

Charge Pump Capacitor Selection

Choose low ESR ($<100\text{m}\Omega$) ceramic capacitors for optimum performance. Low ESR capacitors keep the charge pump output impedance to a minimum, extending the headroom on the negative supply. Choose capacitors with an X7R dielectric for best performance over temperature.

Charge pump load regulation and output resistance is affected by the value of the flying capacitor (C1). A larger valued C1 improves load regulation and minimizes charge pump output resistance. The switch on-resistance and capacitor ESR dominates the output resistance for capacitor values above $2.2\mu\text{F}$.

The output ripple is affected by the value and ESR of the output capacitor (C2). Larger valued capacitors reduce output ripple on the negative power supply. Lower ESR capacitors minimize the output ripple and reduce the output resistance of the charge pump.

Input Capacitor Value Selection

Amplifying the lowest audio frequencies requires high value input coupling capacitor (C_i in [Figure 2](#)). A high value capacitor can be expensive and may compromise space efficiency in portable designs. In many cases, however, the speakers used in portable systems, whether internal or external, have little ability to reproduce signals below 150Hz. Applications using speakers with this limited frequency response reap little improvement by using high value input and output capacitors.

Besides affecting system cost and size, C_i has an effect on the LM4917's click and pop performance. The magnitude of the pop is directly proportional to the input capacitor's size. Thus, pops can be minimized by selecting an input capacitor value that is no higher than necessary to meet the desired –3dB frequency.

As shown in [Figure 2](#), the input resistor, R_i and the input capacitor, C_i, produce a -3dB high pass filter cutoff frequency that is found using Equation (3).

$$f_{i-3dB} = 1 / 2\pi R_i C_i \quad (4)$$

Also, careful consideration must be taken in selecting a certain type of capacitor to be used in the system. Different types of capacitors (tantalum, electrolytic, ceramic) have unique performance characteristics and may affect overall system performance.

AUDIO POWER AMPLIFIER DESIGN

Design a Dual 90mW/16Ω Audio Amplifier

Given:	
Power Output	90mW
Load Impedance	16Ω
Input Level	1Vrms (max)
Input Impedance	20kΩ
Bandwidth	100Hz–20kHz ± 0.50dB

The design begins by specifying the minimum supply voltage necessary to obtain the specified output power. One way to find the minimum supply voltage is to use the Output Power vs Supply Voltage curve in the **Typical Performance Characteristics** section. Another way, using [Equation 5](#), is to calculate the peak output voltage necessary to achieve the desired output power for a given load impedance. For a single-ended application, the result is [Equation 5](#).

$$V_{\text{opeak}} = \sqrt{(2R_L P_O)} \quad (5)$$

$$V_{DD} \geq [2V_{\text{OPEAK}} + (V_{\text{DOTOP}} + V_{\text{DOBOT}})] \quad (6)$$

The Output Power vs Supply Voltage graph for a 16Ω load indicates a minimum supply voltage of 3.1V. This is easily met by the commonly used 3.3V supply voltage. The additional voltage creates the benefit of headroom, allowing the LM4917 to produce peak output power in excess of 90mW without clipping or other audible distortion. The choice of supply voltage must also not create a situation that violates maximum power dissipation as explained above in the **Power Dissipation** section. Remember that the maximum power dissipation point from [Equation 1](#) must be multiplied by two since there are two independent amplifiers inside the package. Once the power dissipation equations have been addressed, the required gain can be determined from [Equation 6](#).

$$A_V \geq \sqrt{(P_O R_L)} / (V_{IN}) = V_{orms} / V_{inrms} \quad (7)$$

Thus, a minimum gain of 1.2 allows the LM4917 to reach full output swing and maintain low noise and THD+N performance. For this example, let $A_V = 1.5$.

The amplifiers overall gain is set using the input (R_i) and feedback (R_f) resistors. With the desired input impedance set at 20k Ω , the feedback resistor is found using Equation (7).

$$A_V = R_f / R_i \quad (8)$$

The value of R_f is 30k Ω .

The last step in this design is setting the amplifier's -3db frequency bandwidth. To achieve the desired ± 0.25 dB pass band magnitude variation limit, the low frequency response must extend to at least one-fifth the lower bandwidth limit and the high frequency response must extend to at least five times the upper bandwidth limit. The gain variation for both response limits is 0.17dB, well within the ± 0.25 dB desired limit. The results are

$$f_L = 100\text{Hz} / 5 = 20\text{Hz} \quad (9)$$

and

$$f_H = 20\text{kHz} \times 5 = 100\text{kHz} \quad (10)$$

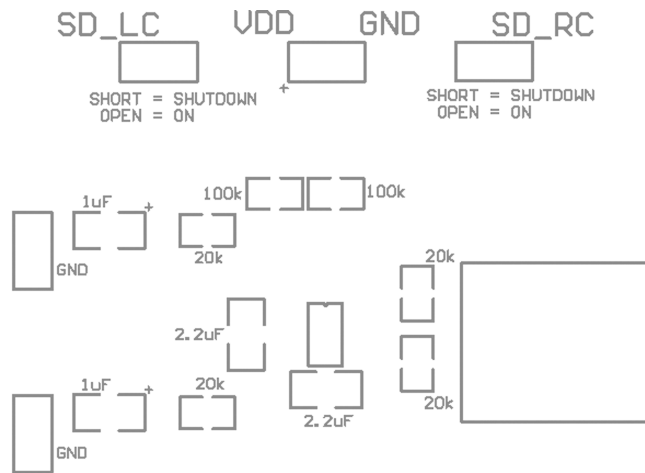
As stated in the **External Components** section, both R_i in conjunction with C_i , and R_L , create first order highpass filters. Thus to obtain the desired low frequency response of 100Hz within ± 0.5 dB, both poles must be taken into consideration. The combination of two single order filters at the same frequency forms a second order response. This results in a signal which is down 0.34dB at five times away from the single order filter -3dB point. Thus, a frequency of 20Hz is used in the following equations to ensure that the response is better than 0.5dB down at 100Hz.

$$C_i \geq 1 / (2\pi * 20\text{k}\Omega * 20\text{Hz}) = 0.397\mu\text{F}; \text{ use } 0.39\mu\text{F} \quad (11)$$

The high frequency pole is determined by the product of the desired high frequency pole, f_H , and the closed-loop gain, A_V . With a closed-loop gain of 1.5 and $f_H = 100\text{kHz}$, the resulting GBWP = 150kHz which is much smaller than the LM4917's GBWP of 3MHz. This figure displays that if a designer has a need to design an amplifier with a higher gain, the LM4917 can still be used without running into bandwidth limitations.

LM4917 SO Demo Board Artwork

Figure 3. Top Overlay



NATIONAL SEMICONDUCTOR
LM4917MT Audio Power Amplifier

Figure 4. Top Layer

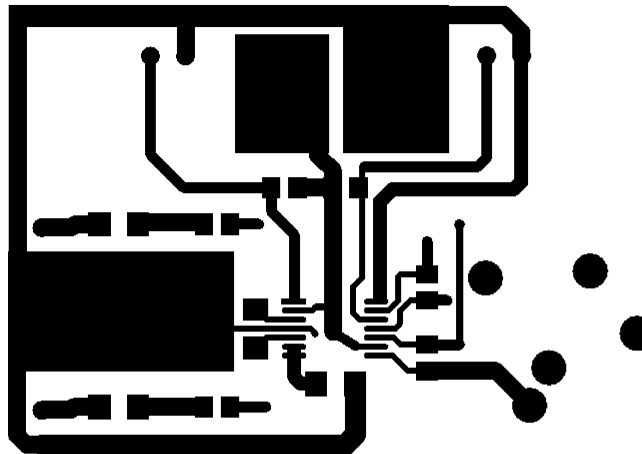
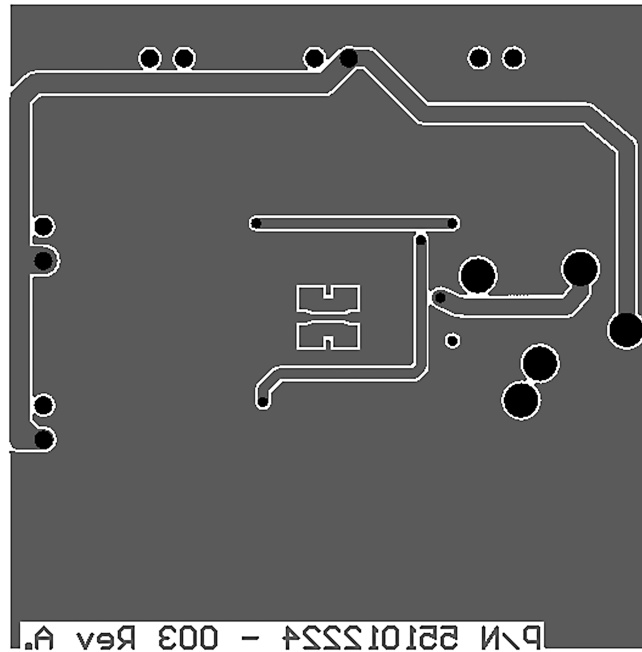


Figure 5. Bottom Layer



PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish	MSL Peak Temp (3)	Samples (Requires Login)
LM4917MT/NOPB	ACTIVE	TSSOP	PW	14	94	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	
LM4917MTX/NOPB	ACTIVE	TSSOP	PW	14	2500	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	
LM4917SD/NOPB	ACTIVE	WSON	NHK	14	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSELETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

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Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM4917MTX/NOPB	TSSOP	PW	14	2500	330.0	12.4	6.95	8.3	1.6	8.0	12.0	Q1
LM4917SD/NOPB	WSO8	NHK	14	1000	178.0	12.4	3.3	4.3	1.0	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS

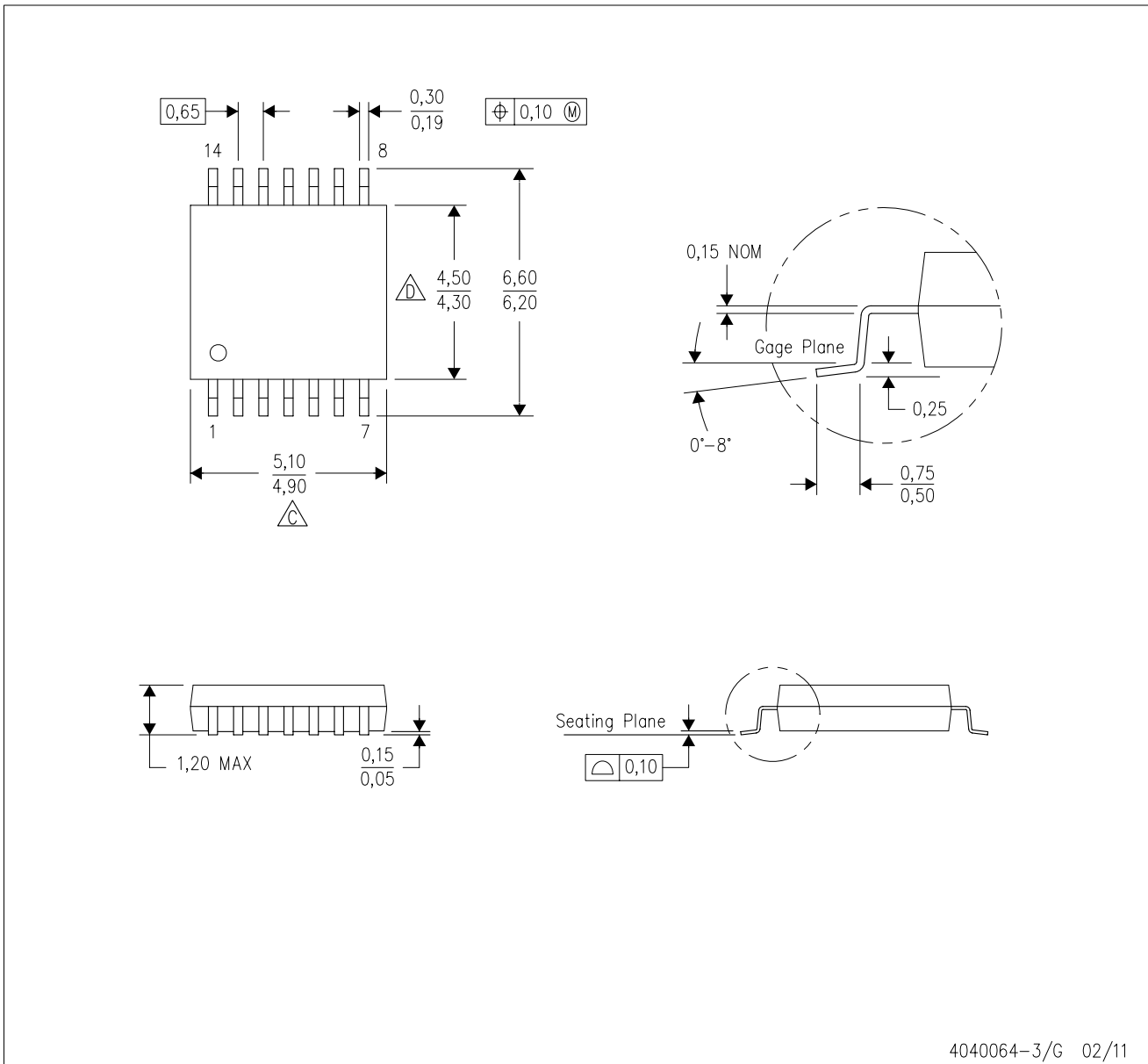

*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM4917MTX/NOPB	TSSOP	PW	14	2500	349.0	337.0	45.0
LM4917SD/NOPB	WSON	NHK	14	1000	203.0	190.0	41.0

MECHANICAL DATA

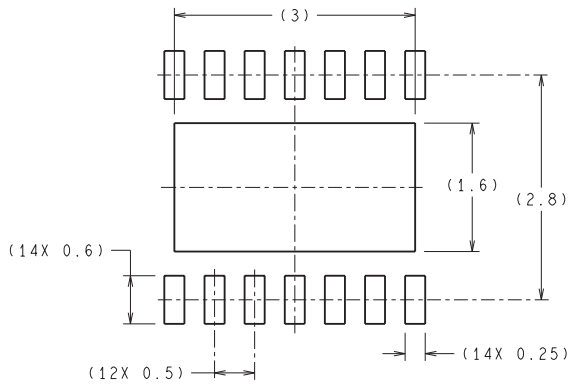
PW (R-PDSO-G14)

PLASTIC SMALL OUTLINE

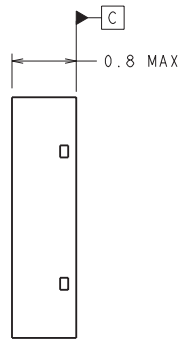
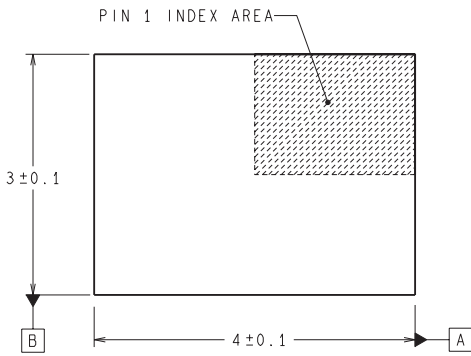


- NOTES:
- A. All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
 - B. This drawing is subject to change without notice.
 - \triangle C Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0,15 each side.
 - \triangle D Body width does not include interlead flash. Interlead flash shall not exceed 0,25 each side.
 - E. Falls within JEDEC MO-153

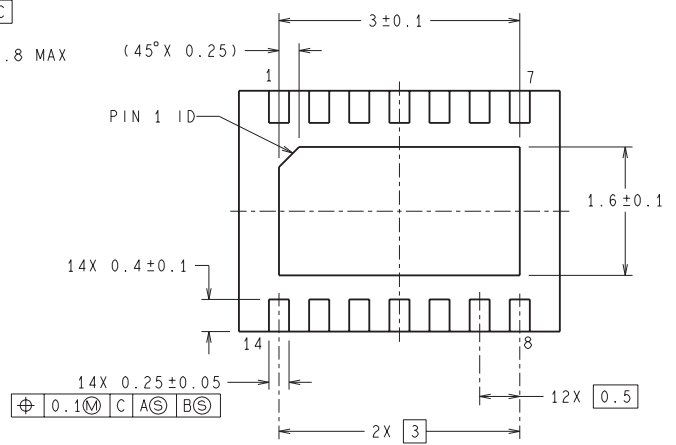
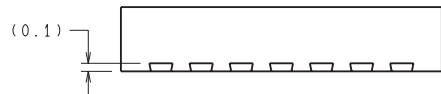
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SDA14A (Rev A)

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