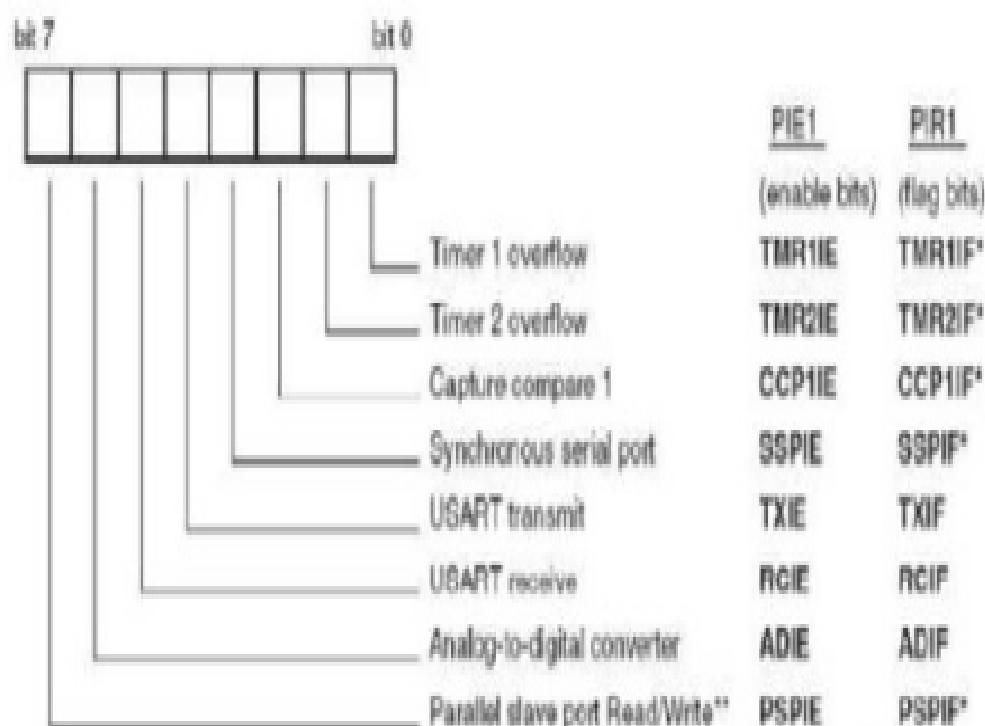


# Peripheral Interrupt Enable and Peripheral Interrupt Request Registers

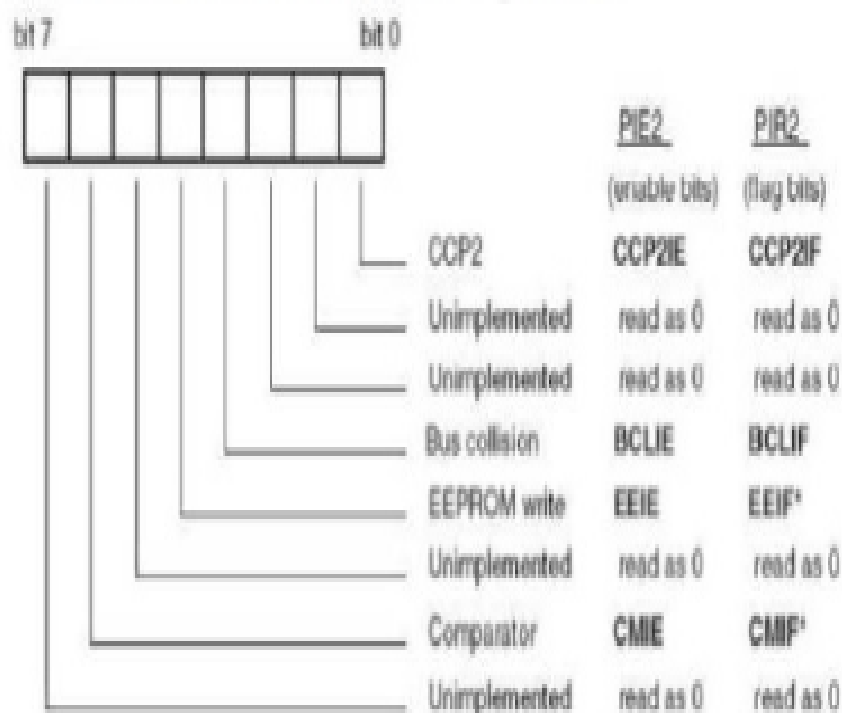
## 16F87XA PIE1/PIR1 Registers



\* Must be cleared in software

\*\* 16F874/7 only. Reserved in 16F873/6

## 16F87XA PIE2/PIR2 Registers



\* Must be cleared in software