CCS Pic-C: Intterupts

To enable or disable interrupts in CCS PIC-C following functions are used ;
enable_interrupts (*level)*
disable_ interrupts (*level)* where *level* is a constant defined in .h file.

These constants are defined the header file of a microcontroller such as "16f84a.h", "16f877a.h". These constants that are used to enable or disable interrupt functions are:

GLOBAL : refer to global level interrupt

INT_RTCC : refer to specific level **TMRO** overflow interrupt (using **RTCC** as the name)

INT_RB : refer to specific level PORTB change interrupt on any RB4, RB5, RB6, RB7 pins

INT_EXT : refer to specific level external interrupt on **RBO-Int pin**

INT_EEPROM : refer to specific level **eeprom** write complete interrupt

INT_TIMERO : refer to specific level **TMRO** overflow interrupt (using **TIMERO** as the name)

INT_AD : refer to specific level **Analog to Digital Conversion** Complete interrupt

INT_TBE : refer to specific level **RS232 transmit** buffer empty interrupt

INT_RDA : refer to specific level **RS232 receive** data available interrupt

INT_TIMER1 : refer to specific level **TMR1** overflow interrupt

INT_TIMER2 : refer to specific level **TMR1** overflow interrupt

INT_CCP1 : refer to specific level **Capture or Compare** interrupt of CCP1 module

INT_CCP2 : refer to specific level **Capture or Compare** interrupt of CCP2 module

INT_SSP : refer to specific level **SPI or I2C** activity interrupt

INT_PSP : refer to specific level **Parallel Slave Port** data in interrupt

INT_BUSCOL : refer to specific level **Bus collision** interrupt

INT_COMP : refer to specific level **Comparator detect** interrupt