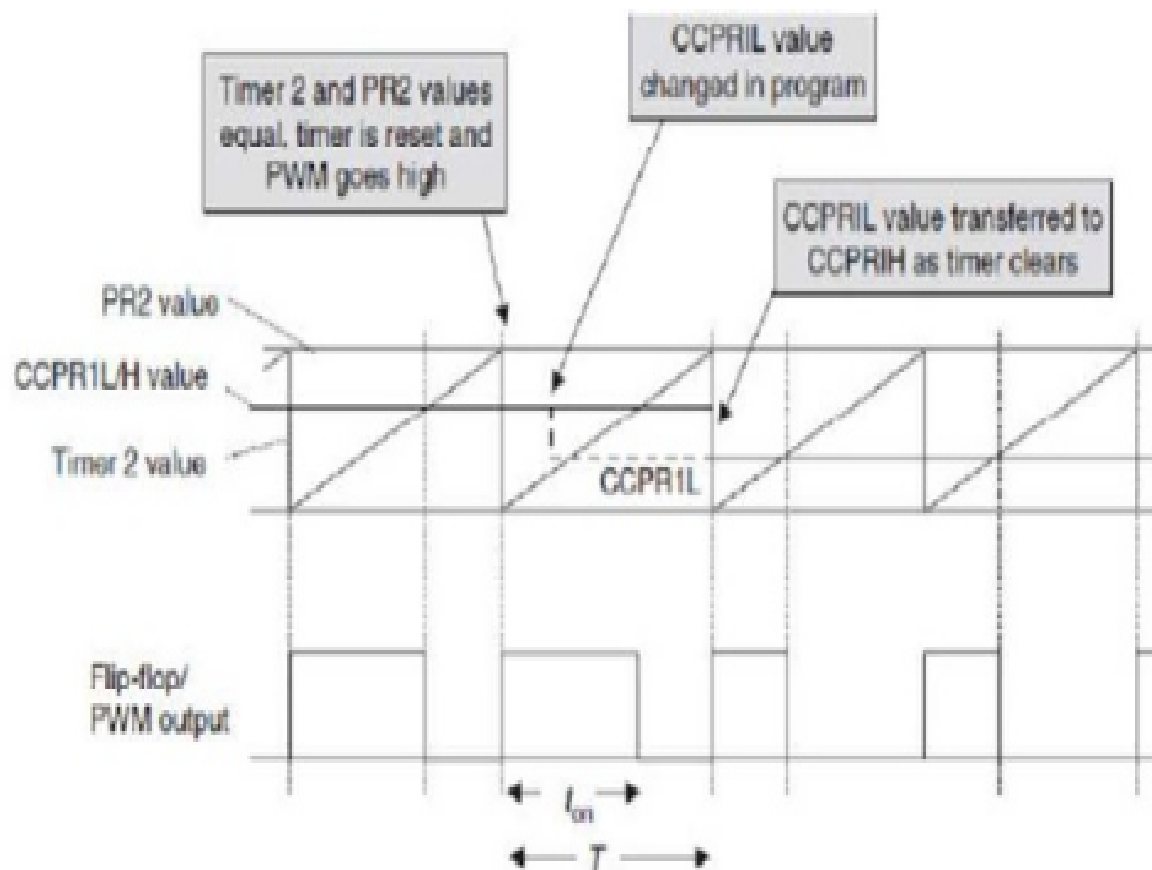# Capture / Compare / PWM (CCP) Modules PWM Signal

$$T = (PR2 + 1) \times (\text{Timer 2 input clock period})$$

$$= (PR2 + 1) \times \{Tosc \times 4 \times (\text{Timer 2 prescale value})\} \qquad (9.2)$$

$$t_{on} = (\text{pulse width register}) \times \{Tosc \times (\text{Timer 2 prescale value})\} \qquad (9.3)$$

PR2 register is loaded with 249_D. clock oscillator frequency is 4 MHz

PWM period is: $T = (PR2 + 1) \times \{Tosc \times 4 \times (\text{Timer 2 prescale value})\}$

$$= 250 \times (250 \, ns \times 4 \times 1)$$

$$= 250 \, \mu s$$

PWM frequency $= 4.00 \, kHz$