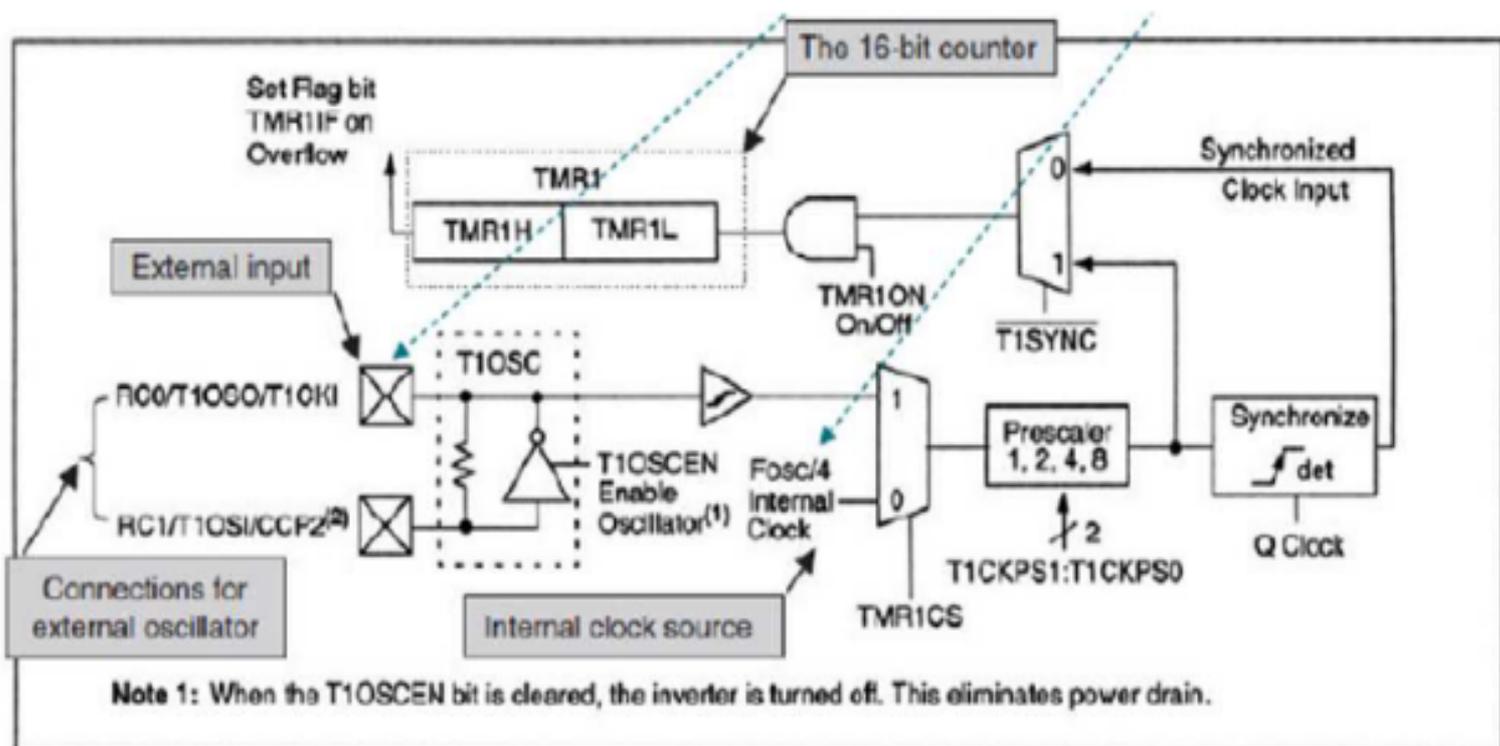
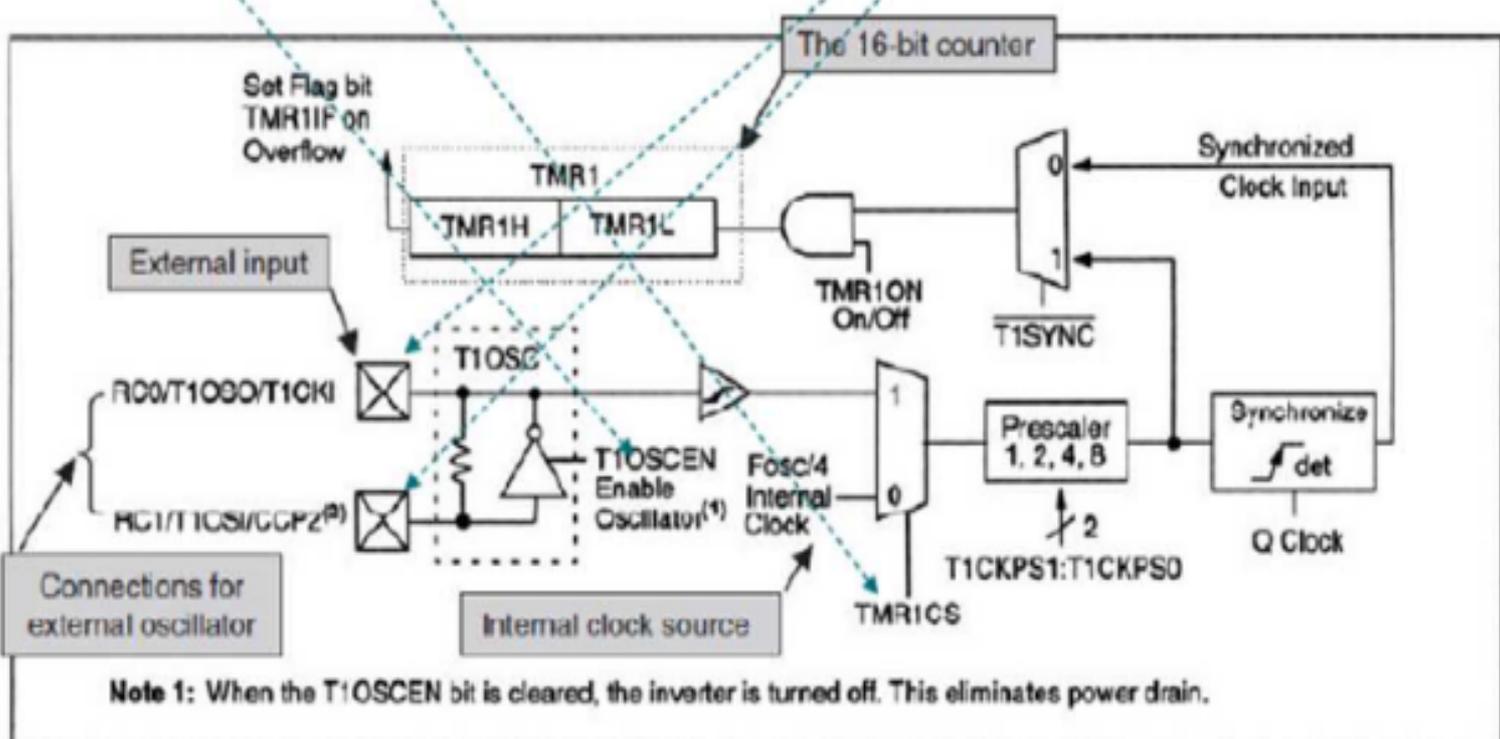


- Timer 1 has three distinct clock sources.
- For counting mode of operation, the external input T1CKI, which is shared with bit0 of PortC, must be used. And in this counting mode, counter always increments on rising edges of source.
- For timing mode of operation, internal clock oscillator (FOSC/4) source can be used.



Timer 1 Block Diagram

- TMR1CS bit of T1CON control register is the external or internal clock source select bit of T1CON control register.
- Connecting a low frequency (i.e up to 200kHz) external oscillator between RC0/T1050 and RC1/T105I pins provides the third option for clock source of TIMER 1 module. This external oscillator can be enabled by the T1OSCEN bit of T1CON.



Timer 1 Block Diagram