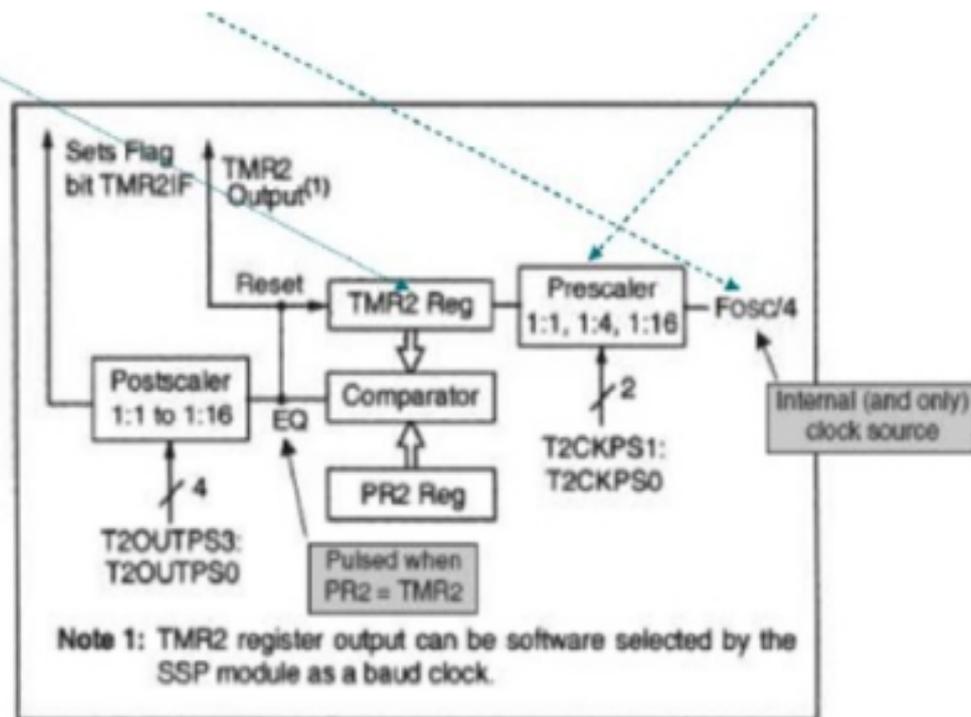


## PIC16F87XA Timer 2, Comparator and PR2 Register

16F87XA Timer 2 module is an 8-bit device. Timer 2 is driven only from the internal oscillator. The 8-bit readable/ writable timer register TMR2 is located at 11<sub>H</sub> in data memory. Modest prescaling (1,4,16) is possible.



Timer 2 Block Diagram

## T2CON control register

U-0	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0	R/W-0
—	TOUTPS3	TOUTPS2	TOUTPS1	TOUTPS0	TMR2ON	T2CKPS1	T2CKPS0
bit 7							bit 0

bit 7 **Unimplemented:** Read as '0'

bit 6-3 **TOUTPS3:TOUTPS0:** Timer2 Output Postscale Select bits

0000 = 1:1 postscale

0001 = 1:2 postscale

0010 = 1:3 postscale

•

•

•

1111 = 1:16 postscale

bit 2 **TMR2ON:** Timer2 On bit

1 = Timer2 is on

0 = Timer2 is off

bit 1-0 **T2CKPS1:T2CKPS0:** Timer2 Clock Prescale Select bits

00 = Prescaler is 1

01 = Prescaler is 4

1x = Prescaler is 16

Postscale bits

Timer2 module can be turned ON or OFF

Prescale bits

Figure 9.5 The Timer 2 control register, T2CON