

```
//Generate a signal at PORTB pin RB0 using timer0 module
#include <16F877A.h>
#define F_CPU 1000000
#define _XTAL_FREQ 1000000
#include <delay.h>
#include <fast_io.h>
#include <timer0.h>
void TIMER0_isr()
{
    output_bit(PIN_B0,0); //RB0 at low voltage level
    delay_ms(100);        //delay 0.1 seconds
}
void main()
{
    set_tris_b(0x00);      //portb pins set as output
    output_b(0x00);        //clear portb data
    set_timer0(0); //set TMR0 initial value to zero
    setup_timer_0(RTCC_INTERNAL_RTCC_DIV_256); //set timer0 mode
    enable_interrupts(INT_TIMER0); //enable timer overflow interrupt
    enable_interrupts(GLOBAL); //enable global interrupt
    while(TRUE)
    {
        output_bit(PIN_B0,1); //RB0 at high voltage level
    }
} //end of main
```