



SPECIFICATION CHARACTER TYPE DOT MATRIX LCD MODULE



ITEM NUMBER: FDCG12864H-FLYYBW-91BN

ESTABLISHED DATE: 2010.12

DATASHEET VERSION: 2008 VERSION

ISSUED BY: 魏蘇东 CHECKED BY: 紫葵 APPROVED BY: 紫葵

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STANDARD DOC.

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FORDATA's 2006 version logo. FORDATA is an integrated manufacturer of flat panel display (FPD). FORDATA supplies TN, HTN, STN, FSTN monochrome LCD panel; COB, COG, TAB LCD module; and all kinds of LED backlight.

classic mono LCDs



FAST RESPONSE TIME

This icon on the cover indicates the product is with high response speed; Otherwise not.



PROTECTION CIRCUIT

This icon on the cover indicates the product is with protection circuit; Otherwise not.



HIGH CONTRAST

This icon on the cover indicates the product is with high contrast; Otherwise not.



LONG LIFE VERSION

This icon on the cover indicates the product is long life version (over 9K hours guaranteed); Otherwise not.



WIDE VIEWING SCOPE

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OPERATION TEMPERATURE RANGE

This icon on the cover indicates the operating temperature range (X-Y).



3TIMEs 100% QC EXAMINATION

This icon on the cover indicates the product has passed FORDATA's thrice 100% QC. Otherwise not.



TWICE SELECTION OF LED MATERIALS

This icon on the cover indicates the LED had passed FORDATA's twice strict selection which promises the product's identical color and brightness; Otherwise not.



Vlcm = 3.0V

This icon on the cover indicates the product can work at 3.0V exactly; otherwise not.



N SERIES TECHNOLOGY (2008 developed)

FORDATA adopts new structure, new craft, new technology and new materials inside both LCD module and LCD panel to improve the "RainBow"



REVISION RECORD PAGE 1/17

יוך	PROFESSIONAL LCI	D SUPPLIER FROM CHINA DOC		EVISION	RECORD	PAGE 1/1	
NO.	DATE	DESCRIPTION			PAGE	APPROVE	
1	2010.12	INITIAL ISSUED		ALL	ALL	Style.	



NUMBER SYSTEM

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1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
FD	С	С	08	01	A	F	L	Y	Y	В	W	5	2	L	E

No.	REMARKS	DESCRIPTION
1	COMPANY ABBRAVIATED	FD = FORDATA
2	IC packing	C = Chip On Board G = Chip On Glass T = TAB
3	LCM type	C = Character G = Graphic
4	Chyaracter	08, 10, 12, 16, 20, 24, 40, = Character number Per line
4	Graphic	80, 100, 120, 122, 128, 160 = Row Dots Quantity
5	Character	01, 02, 04, = Character Lines
5	Graphic	32, 64, 80, 128, 160 =Column Dots Quantity
6	Serial Number	A~Z
7	Polarizer type	R = Positive Reflective M = Positive Transmissive E = Negative, Transflective B = Negative, Dual optical compensation (for FSTN type only)
8	Backlight type	N = No Backlight S = Edge Type LED Backlight L = Array Type LED Backlight S = Edge Type LED Backlight E = EL backlight without Invertor F = EL backlight with Invertor C = CCFL backlight without Invertor T = CCFL backlight with Invertor
9	Backlight color	N = No BacklightY = Yellow-GreenW = WhiteR = RedA = AmberC = Blue-GreenB = BlueG = Green
10	LCD panel type	T = TN $H = HTN$ $Y = Yellow-Green STN$ $G = Gray STN$ $B = Blue STN$ $F = FSTN$
11	Viewing angle	B = Bottom 6:00 T = Top 12:00 R = Right 3:00 L = Left 9:00
12	Operation temperature range	$S = 0^{\circ}C \sim 50^{\circ}C$ (Single Supply Voltage) $D = 0^{\circ}C \sim 50^{\circ}C$ (Dual Supply Voltage) $W = -20^{\circ}C \sim 70^{\circ}C$ (Single Supply Voltage) $H = -20^{\circ}C \sim 70^{\circ}C$ (Dual Supply Voltage) $T = -30^{\circ}C \sim 80^{\circ}C$ (Single Supply Voltage) $E = -30^{\circ}C \sim 80^{\circ}C$ (Dual Supply Voltage)
13	Driving Voltage	1: VIcm = 3.0V, No / EL / CCFL Backlight or VIcm = 3.0V, VIed = LED voltage, (Via AK) 2: VIcm = 3.6V, VIed = 5.0V (Not via AK) 3: VIcm = 3.6V, VIed = LED voltage, (Not via AK) 4: VIcm = 5.0V, VIed = LED voltage, (Not via AK) 5: VIcm = 5.0V, VIed = 5.0V (Not via AK) 6: VIcm = 5.0V, No / EL / CCFL Backlight or VIcm = 5.0V, VIed = LED voltage, (Via AK) 7: VIcm = 3.6V, No / EL / CCFL Backlight or VIcm = 3.6V, VIed = LED voltage, (Via AK) 8: VIcm = 3.0V, VIed = 5.0V 9: VIcm = 3.0V, VIed = LED voltage, (Not via AK)
14	Backlight Connect Method	0 = PIN1 LED-, PIN2 LED+ 1 = PIN15(17/19) LED+, PIN16(18/20) LED- 2 = PIN15(17/19) LED-, PIN16(18/20) LED+ 3 = PIN15(17/19) LED+, PIN16(18/20) NC 4 = PIN15(17/19) NC, PIN16(18/20) LED+ 5 = PINA LED+, PINK LED- 6 = No / EL / CCFL Backlight
15	IC Manufacturer	X = SAMSUNG L = SUNPLUS S = SITRONIX T = TOSHIBA E = EPSON H = HOLTEK Q = ASLIC N = CIMTEK P = PRINCETON
16	Font Set	R = English - Russia E = English - Japanese U = English - Europe H = English - Hebrew K = English - Europe N = NO FONT SET



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1. GENERAL SPECIFICATIONS

ITEM	NOMINAL DIMENSIONS / AVAILABLE OPTIONS
DISPLAY FORMAT	128 X 64 DOT MATRIX
LCD PANEL OPTIONS	STN (Yellow-green color)
POLARIZER OPTIONS	Positive, Transflective
BACKLIGHT OPTIONS	Array type LED backlight (Yellow-green color)
VIEWING ANGLE OPTIONS	6:00 (Bottom)
TEMPERATURE RANGE OPTIONS	Wide temp. range (- 20°C ~ 70°C)
CONTROLLERIC	NT7107C+NT7108C
NEGATIVE IC	Not Built In
DISPLAY DUTY	1/64
DRIVING BIAS	1/9

2. MECHANICAL SPECIFICATIONS

OVERALL SIZE	LED backlight	versi	on: 75.0 x 52.7 x	75.0 x 52.7 x max 13.0		
VIEWING AREA	60.0W x 32.5H	mm	HOLE-HOLE	70.0W x 49.7H	mm	
DOT SIZE	0.39W x 0.39H	mm	DOT PITCH	0.04W x 0.04H	mm	
WEIGHT (EL BKL)		g	WEIGHT (LED BKL)	65.0	g	

3. ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITION	MIN	MAX	UNIT
POWER SUPPLY (LOGIC)	Vdd	25°C	-0.3	7.0	V
POWER SUPPLY (LCD)	V0	25°C	Vdd -19.0	Vdd +0.3	V
INPUT VOLTAGE	Vin	25°C	-0.3	Vdd +0.3	V
OPERATING TEMPERATURE	Vopr		-20	70	°C
STORAGE TEMPERATURE	Vstg		-30	80	°C

4. ELECTRONICAL CHARACTERISTIC*

ITEM	CVMPOL	CONDITION	S	FANDA	RD	UNIT
I I EW	SYMBOL	CONDITION	MIN	TYP	MAX	UNII
Input voltage	Vdd	+3V	2.7	3.0	5.5	٧
Supply current	ldd	Vdd=3V		2.1		mA
		-20°C				
Recommended LCD driving		0°C				
voltage for normal temp.	Vdd - V0	25°C				V
Version module		50°C				
		70°C				
LED forward voltage	Vf	25°C	3.8	4.1	4.4	٧
LED forward current	If	25°C		275		mA
LED reverse Current	lr	25°C			1400	μA
LED Peak wave length	λр	25°C If = 275mA	568		575	nm
LED illuminance (Without LCD)	Lv	25°C If = 275mA	180	225		cd/m²
LED life time		25°C If = 275mA	9K**			Hours

^{*} The above data are for reference only.

^{**} The warranty period of FORDATA LCD module is 1YEAR counted from the date shown on the label of products.



SPEC.

MODE NO. FDCG12864H-FLYYBW-91BN

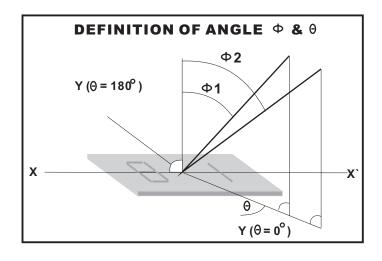
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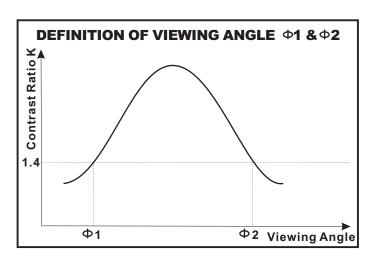
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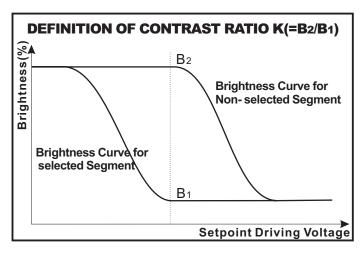
5. OPTICAL CHARACTERISTIC

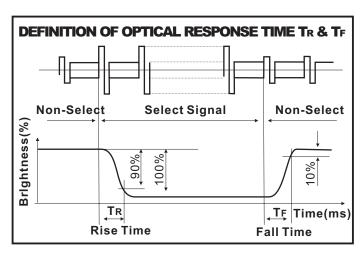
FOR TN TYPE LCD MODULE (TA=25°C, Vdd=3.0V ± 0.25V)									
ITEM SYMBOL CONDITION MIN TYP MAX UNIT									
VIEWING ANGLE	Ф2-Ф 1	K=4	30			deg			
VIEWING ANGLE	Θ	N-4	25						
CONTRAST RATIO	K			2					
RESPONSE TIME(RISE)	T R			120	150	ms			
RESPONSE TIME(FALL)	TF			120	150	ms			

FOR STN TYPE LCD MODULE (TA=25 °C, Vdd=3.0V ± 0.25V)									
ITEM SYMBOL CONDITION MIN TYP MAX UNIT									
VIEWING ANGLE	Ф2-Ф 1	K=4	40			deg			
VIEWING ANGLE	Θ	N=4	60			ueg			
CONTRAST RATIO	K			6					
RESPONSE TIME(RISE)	TR			150	250	ms			
RESPONSE TIME(FALL)	T F			150	250	ms			











6. DC CHARACTERISTIC

(Unless otherwise stated, VDD= +5V ± 10%, VSS=0V, Ta=25°C)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
High Level Input Voltage	VIH1		0.7VDD		VDD	V
Trigit Level input voitage	VIH2		0.7VDD		VDD	V
Low Level Input Voltage	VIL1		0		0.3VDD	V
Low Level Input voltage	VIL2		0		0.8	V
High Level Output Voltage	VOH	IOH = - 200µA	2.4			V
Low Level Output Voltage	VOL	IOL = 1.6 mA			0.4	V
Input Leakage Current	ILKG	VIN = VDD to VSS	-1.0		1.0	μ A
Three-State (OFF) Input Current	ITSL	VIN = VDD to VSS	-5.0		5.0	μ А
	IDD1	During Display			100	μ А
Operating Current	IDD2	During Access, Access Cycle = 1MHz			500	μ Α

7. AC CHARACTERISTIC

Characteristic	Symbol	Min	Тур	Max	Unit
E Cycle	tc	1000	_	_	ns
E High Level Width	t wн	450	_	_	ns
E Low Level Width	t wL	450	_	_	ns
E Rise Time	t _R	_	_	25	ns
E Fall Time	t _F	_	_	25	ns
Address Setup Time	t ASU	140	_	_	ns
Address Hold Time	t AH	10	_	_	ns
Data Setup Time	t DSU	200	_	_	ns
Data Delay Time	t _D	_	_	320	ns
Data Hold Time (Write)	t DHW	10	_	_	ns
Data Hold Time (Read)	t DHR	20	_	_	ns

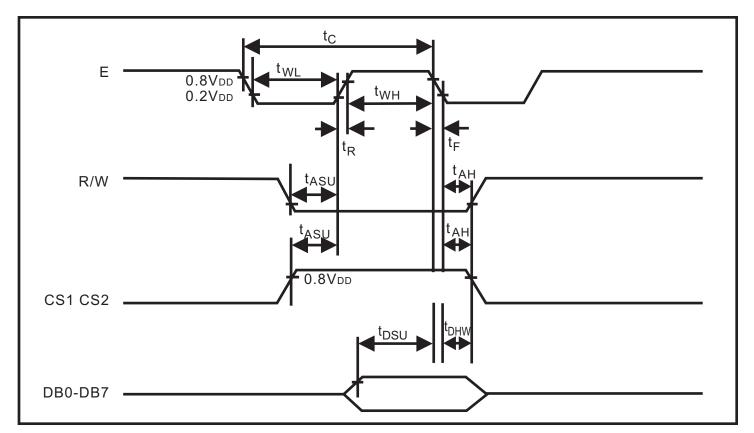


SPEC.

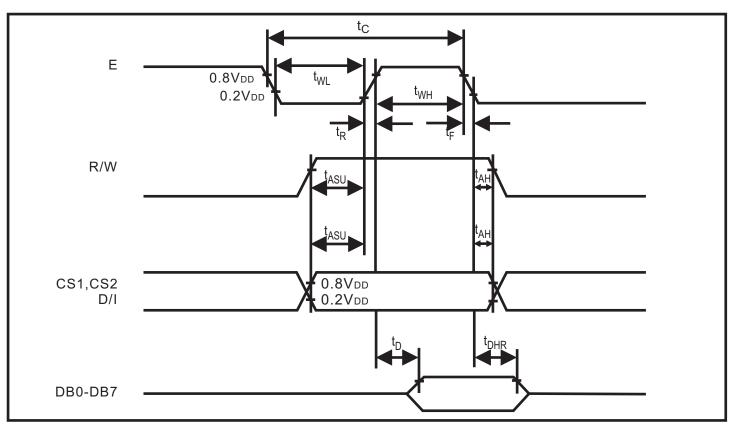
MODE NO. FDCG12864H-FLYYBW-91BN

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7.1 WRITE MODE TIMING DIAGRAM



7.2 READ MODE TIMING DIAGRAM



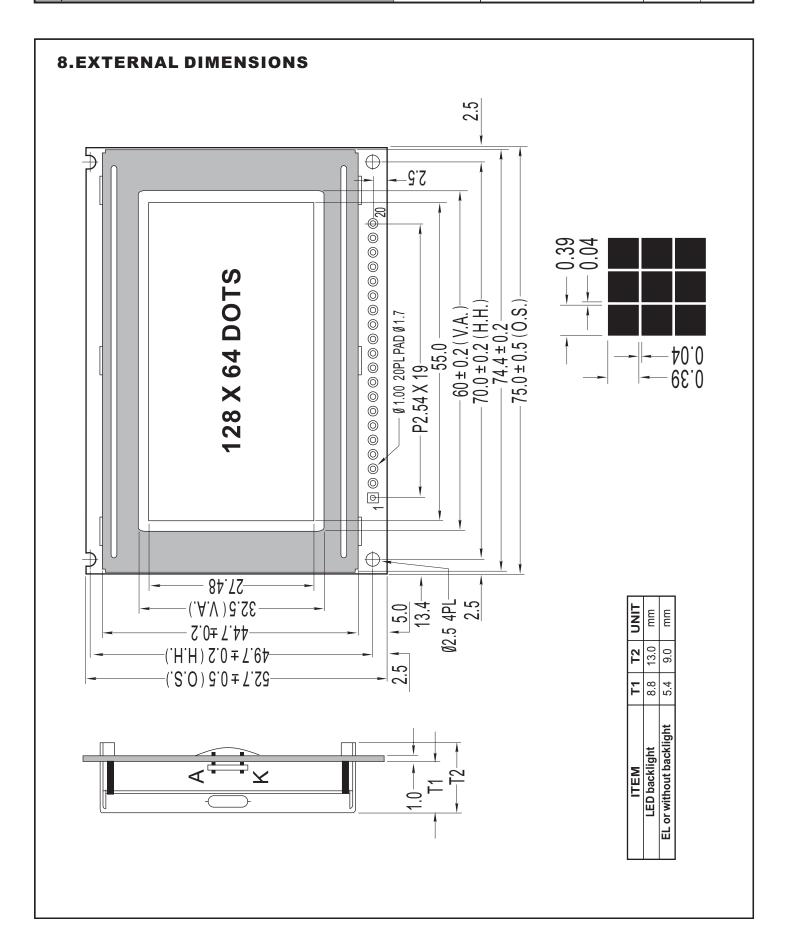


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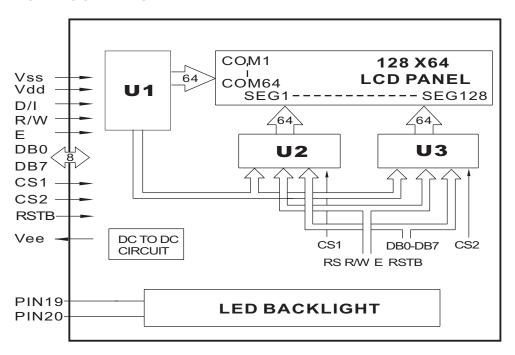
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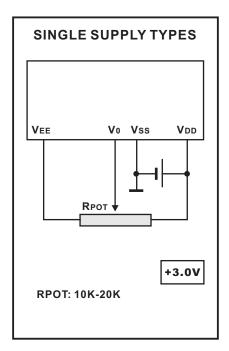
9. PIN ASSIGNMENT

PIN NO.	SYMBOL	FUN	ICTION	REMARK		
1	Vdd		+3V			
2	Vss	Power Supply	0V			
3	V0		Contrast Adjust			
4	DB0	Dat	Data Bit 0			
5	DB1	Dat	a Bit 1			
6	DB2	Dat	a Bit 2			
7	DB3	Dat	Data Bit 3			
8	DB4	Dat				
9	DB5	Dat	a Bit 5			
10	DB6	Dat				
11	DB7	Dat	a Bit 7			
12	CS1	When CS1=H,	CS2=L, select U2			
13	CS2	When CS1=L,	CS2=H, select U3			
14	RSTB	Rese	et signal			
15	R/W	Read	d / Write			
16	D/I	H/L H: Data, L	: Instruction code			
17	E	Chip Enable signal				
18	Vee	Negative voltage output				
19	LED+	Anode o	4.2V			
20	LED-	Cathode	0V			

10. BLOCK DIAGRAM



11. POWER SUPPLY





12. FUNCTIONAL DESCRIPTION

12.1 RESET

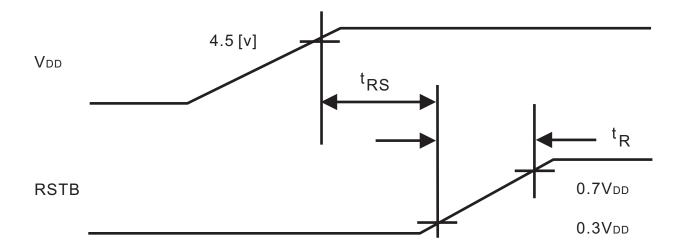
The system can be initialized by setting the RSTB to LOW when turning the power ON or by instruction from the MPU. When the RSTB is set to LOW, the following condition occurs:

- 1. The Display is turned OFF.
- 2. The Display Start Line register is set to 0 (Z-Address 0).

No instructions except the status read can be executed when the RSTB is LOW. This means that in order to execute other instructions, the RSTB must be cleared by setting DB4 to 0 and the DB7 set to 0 by status read instruction.

The table below shows the power supply initial conditions.

Parameter	Symbol	Min.	Тур.	Max.	Unit
Reset Time	tRS	1.0	-	-	uS
Rise Time	tR	-	-	200	nS





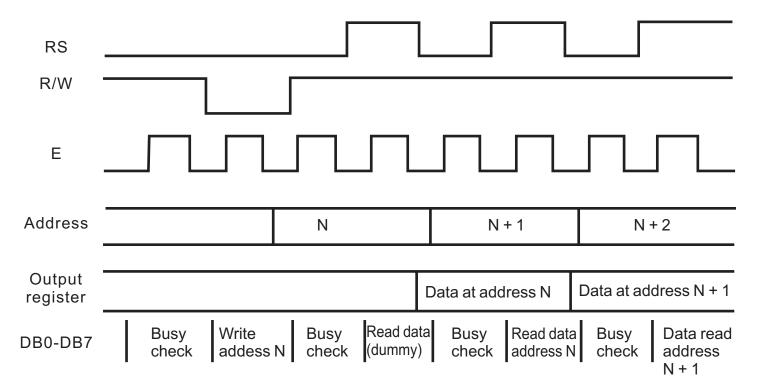
PRODUCT SPEC.

MODE NO. FDCG12864H-FLYYBW-91BN

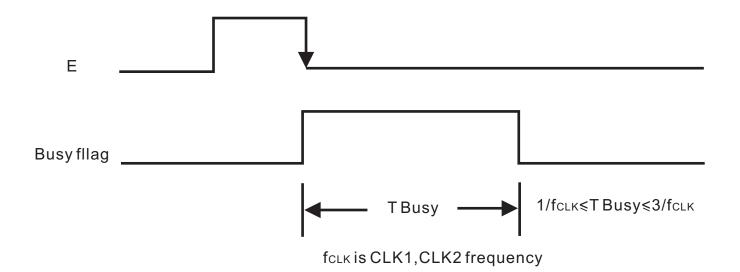
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12.2 BUSY FLAG

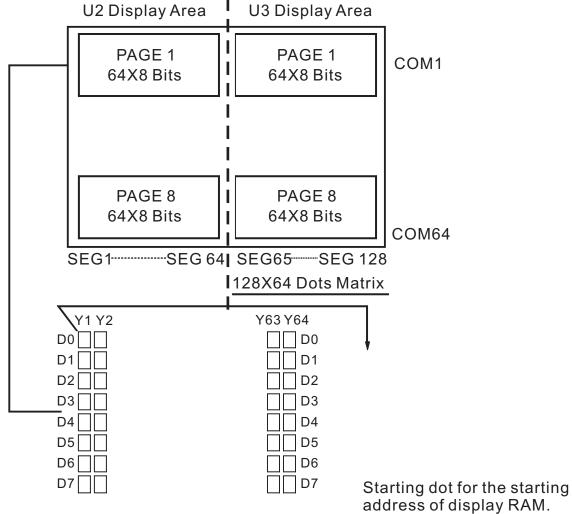
The busy flag (DB7) is used to determine whether Nt7108 is operating or not. When the busy flag is HIGH, internal operation is taking place. When the busy flag is LOW, Nt7108 can accept data or instructions. The busy check diagram is shown below.



The busy flag diagram is shown below.



12.3 RELATION BETWEEN DISPLAY PATTERN AND DRIVERS



Each segment driver has 8 pages RAM, and each page has 64x8 bits RAM. D0~D7 are 8 bits transmitted data, where D0 is LSB and D7 is MSB.

12.4 DISPLAY DATA RAM

The Display Data RAM is used to store the display data for the liquid crystal display. Write data 1 is indicates an ON State of the LCDs dot matrix while the OFF State is written as 0. ADC Signal can control the Display Data RAM and the segment output. Please refer to the table below.

ADC *	Display Data
Н	Y-Address 0:S1 to Y-Address 63:S64



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13. INSTRUCTION

	1										
Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display ON/OFF	L	L	L	L	Н	Н	Н	Н	Н	L/H	Controls the display on or off. Internal status and display RAM data is not affected. L:OFF H:ON
Set address (Y address)	L	L	L	П	Y address (0~63)					Sets the Y address in the Y address counter.	
Set Page (X address)	L	L	Н	L	Н	Н	Н	Pa	ge (0-	-7)	Sets the X address at the X address register.
Display Start Line (Z address)	L	L	Н	Н		Disp	lay sta	rt line (0~63)			Indicates the display data RAM displayed at the top of the screen.
Status Read	L	Н	B U S Y	L	O N / O F F	R E S E T	L	L	L	L	BUSY L:Ready H:In operation ON/OFF L:Display ON H:Display OFF RESET L:Normal H:Reset
Write Display Data	Н	L			(DB0: displa RAM, Write Data writ instruct addr increas				Writes data (DB0:7) into display data RAM,After writing instruction,Y address is increased by 1 automatically.		
Read Display Data	Н	Н			Reads data (DB0:7) from Read Data display data RAM to the data bus.						



14. DESCRIPTION OF COMMAND

Display On/Off

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	0	1	1	1	1	1	D

The display data appears when D is 1 and disappears when D is 0.

Though the data is not on the screen with D=0, it remains in the display data RAM.

Therefore, you can make it appear by changing D=0 into D=1.

Set Address(Y Address)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

Y address (AC0-AC5) of the display data RAM is set in the Y address counter. An address is set by instruction and increased by 1 automatically by read or write operations of display data.

Set Page(X Address)

_	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
	0	0	1	0	1	1	1	AC2	AC1	AC0

X address (AC0-AC2) of the display data RAM is set in the X address register. Writing or reading to or from MPU is executed in this specified page until the next page is set.

Display Start Line(Z Address)

	R/W									_
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0	

Z address (AC0-AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen.

When the display duty cycle is 1/64 or others(1/32-1/64), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.



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Status Read

_	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	Db1	DB0
	0	1	BUSY	0	ON/OFF	RESET	0	0	0	0

BUSY

When BUSY is 1, the Chip is executing internal operation and no instructions are accepted. When BUSY is 0,the Chip is ready to accept any instructions.

ON/OFF

When ON/OFF is 1,the display is off. When ON/OFF is 0,the display is on.

RESET

When RESET is 1,the system is being initialized.

In this condition, no instructions except status read can be accepted.

When RESET is 0, initializing has finished and the system is in the usual operation condition.

Write Display Data

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D7	D6	D5	D4	D3	D2	D1	D0

Writes data (D0-D7) into the display data RAM.

After writing instruction, Y address is increased by 1 automatically.

Read Display Data

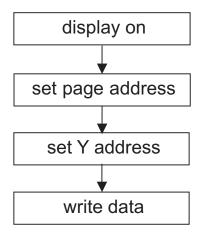
RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	1	D7	D6	D5	D4	D3	D2	D1	D0

Reads data (D0-D7) from the display data RAM.

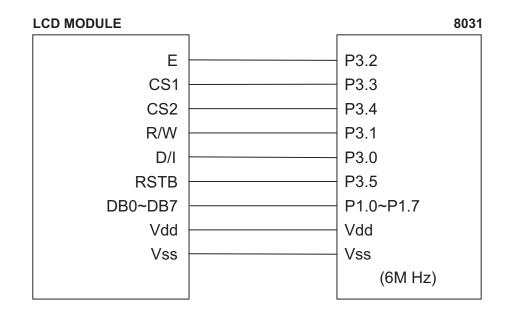
After reading instruction, Y address is increased by 1 automatically.

15. APPLICATION EXAMPLE

Application Flowchart



Application Circuit





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16. PACKING DETAIL

WITH LED BKL
45 PCS/BOX
8 BOXES/CARTON
360 PCS/CARTON
19.00 KGS/CTN(G.W.)

0.07 M³/CARTON

WITHOUT LED BKL 45 PCS/BOX 8 BOXES/CARTON 360 PCS/CARTON 17.00 KGS/CTN(G.W.) 0.07 M³/CARTON

NOTE

- 1. The weight is estimated for reference only.
- 2. Packing detail may be changed without notice.

