

PIC16F84 Configuration Bits

PIC16F84 Configuration Bits					
13 _____ 4 3 2 1 0 ----- Code protect Osc type 11 = RC 0 = on 10 = HS 1 = off 01 = XT 0 = disabled 00 = LP 1 = enabled Power-up timer 0 = enabled 1 = disabled				_CP_ON EQU H' 000F' 1111 _CP_OFF EQU H' 3FFF' 1111 _PWRTE_ON EQU H' 3FF7' 0111 _PWRTE_OFF EQU H' 3FFF' 1111 _WDT_ON EQU H' 3FFF' 1111 _WDT_OFF EQU H' 3FFB' 1011 _LP_OSC EQU H' 3FFC' 1100 _Xt_OSC EQU H' 3FFD' 1101 _HS_OSC EQU H' 3FFE' 1110 _RC_OSC EQU H' 3FFF' 1111	
_CP_OFF 0011 1111 1111 1111 (3FFFh) _PWRTE_ON 0011 1111 1111 0111 (3FF7h) _WDT_OFF 0011 1111 1111 1011 (3FFBh) _HS_OSC 0011 1111 1111 1110 (3FFEh)		_CP_OFF 0011 1111 1111 1111 (3FFFh) _PWRTE_OFF 0011 1111 1111 1111 (3FFFh) _WDT_OFF 0011 1111 1111 1011 (3FFBh) _HS_OSC 0011 1111 1111 1110 (3FFEh)		_CP_OFF 0011 1111 1111 1111 (3FFFh) _PWRTE_ON 0011 1111 1111 0111 (3FF7h) _WDT_ON 0011 1111 1111 1111 (3FFFh) _HS_OSC 0011 1111 1111 1110 (3FFEh)	
Result 0011 1111 1111 0010 (3FF2h)		Result 0011 1111 1111 1010 (3FFAh)		Result 0011 1111 1111 0110 (3FF6h)	
_CP_OFF 0011 1111 1111 1111 (3FFFh) _PWRTE_ON 0011 1111 1111 0111 (3FF7h) _WDT_OFF 0011 1111 1111 1011 (3FFBh) _XT_OSC 0011 1111 1111 1101 (3FFDh)		_CP_OFF 0011 1111 1111 1111 (3FFFh) _PWRTE_OFF 0011 1111 1111 1111 (3FFFh) _WDT_OFF 0011 1111 1111 1011 (3FFBh) _XT_OSC 0011 1111 1111 1101 (3FFDh)		_CP_OFF 0011 1111 1111 1111 (3FFFh) _PWRTE_ON 0011 1111 1111 0111 (3FF7h) _WDT_ON 0011 1111 1111 1111 (3FFFh) _XT_OSC 0011 1111 1111 1101 (3FFDh)	
Result 0011 1111 1111 0001 (3FF1h)		Result 0011 1111 1111 1001 (3FF9h)		Result 0011 1111 1111 0101 (3FF5h)	
_CP_OFF 0011 1111 1111 1111 (3FFFh) _PWRTE_ON 0011 1111 1111 0111 (3FF7h) _WDT_OFF 0011 1111 1111 1011 (3FFBh) _RC_OSC 0011 1111 1111 1111 (3FFFh)		_CP_OFF 0011 1111 1111 1111 (3FFFh) _PWRTE_OFF 0011 1111 1111 1111 (3FFFh) _WDT_OFF 0011 1111 1111 1011 (3FFBh) _RC_OSC 0011 1111 1111 1111 (3FFFh)		_CP_OFF 0011 1111 1111 1111 (3FFFh) _PWRTE_ON 0011 1111 1111 0111 (3FF7h) _WDT_ON 0011 1111 1111 1111 (3FFFh) _RC_OSC 0011 1111 1111 1111 (3FFFh)	
Result 0011 1111 1111 0011 (3FF3h)		Result 0011 1111 1111 1011 (3FFBh)		Result 0011 1111 1111 0111 (3FF7h)	
_CONFIG _RC_OSC & _PWRTE_ON & _WDT_OFF & _CP_OFF ; (3FF3h) _CONFIG _RC_OSC & _PWRTE_OFF & _WDT_ON & _CP_OFF ; (3FFFh) _CONFIG _RC_OSC & _PWRTE_OFF & _WDT_OFF & _CP_OFF ; (3FFBh) _CONFIG _RC_OSC & _PWRTE_ON & _WDT_ON & _CP_OFF ; (3FF7h)		_CONFIG _XT_OSC & _PWRTE_ON & _WDT_OFF & _CP_OFF ; (3FF1h) _CONFIG _XT_OSC & _PWRTE_OFF & _WDT_ON & _CP_OFF ; (3FFDh) _CONFIG _XT_OSC & _PWRTE_OFF & _WDT_OFF & _CP_OFF ; (3FF9h) _CONFIG _XT_OSC & _PWRTE_ON & _WDT_ON & _CP_OFF ; (3FF5h)			
_CONFIG _HS_OSC & _PWRTE_ON & _WDT_OFF & _CP_OFF ; (3FF2h) _CONFIG _HS_OSC & _PWRTE_OFF & _WDT_ON & _CP_OFF ; (3FFEh) _CONFIG _HS_OSC & _PWRTE_OFF & _WDT_OFF & _CP_OFF ; (3FFAh) _CONFIG _HS_OSC & _PWRTE_ON & _WDT_ON & _CP_OFF ; (3FF6h)		_CONFIG _LP_OSC & _PWRTE_ON & _WDT_OFF & _CP_OFF ; (3FF0h) _CONFIG _LP_OSC & _PWRTE_OFF & _WDT_ON & _CP_OFF ; (3FFCh) _CONFIG _LP_OSC & _PWRTE_OFF & _WDT_OFF & _CP_OFF ; (3FF8h) _CONFIG _LP_OSC & _PWRTE_ON & _WDT_ON & _CP_OFF ; (3FF4h)			

<http://www.mstracey.btinternet.co.uk/pictutorial/picmain.htm>
http://www.massmind.org/images/www/hobby_elec/e_pic.htm

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