

版本号	变更记录	日期	签名	批准
A				

序号	物料编号	名称	材料及规格	数量	备注
30	1-2160-0182-0000	机脚垫		4	
29	1-2220-0034-0000	绝缘片		1	
28	1-8600-0286-0001	AV板		1	
27	1-8600-0058-0003	SCAR板		1	
26	1-8600-6125-0004	解码板		1	
25	1-2020-A209-0000	后板		1	
24	1-4016-2000-0G05	电源线		1	
23	1-2134-3006-0002	螺丝		11	
22	1-2130-3006-0011	螺丝		5	
21	1-2010-N868-0005	上盖		1	
20	1-2220-0020-0000	绝缘片		1	
19	1-2161-0033-0000	海绵		2	
18	1-2134-3006-0004	螺丝		4	
17	1-2134-3008-0000	螺丝		2	
16	1-1240-2303-0003	机芯		1	
15	1-2160-0074-0000	机芯胶垫		2	
14	1-2070-N868-0003	MIC支架		1	
13	1-8600-0258-0002	MIC板		1	
12	1-2000-N868-0002	底板		1	
11	1-2071-N866-0000	面壳支架		1	
10	1-8600-1287-0003	KB板		1	
9	1-2030-A209-0000	面板		1	
8	1-205A-N866-0000	功能钮		1	
7	1-2040-N866-0000	仓门		1	
6	1-2050-K256-0000	电源钮		1	
5	1-2064-K256-0001	导光块		1	
4	1-2060-K256-0002	装饰条		1	
3	1-2240-A209-0000	PVC镜片		1	
2	1-2060-A209-0000	上装饰块		1	
1	1-2060-A209-0001	下装饰块		1	

材料: EXPLODED VIEW

表面处理: P/N :

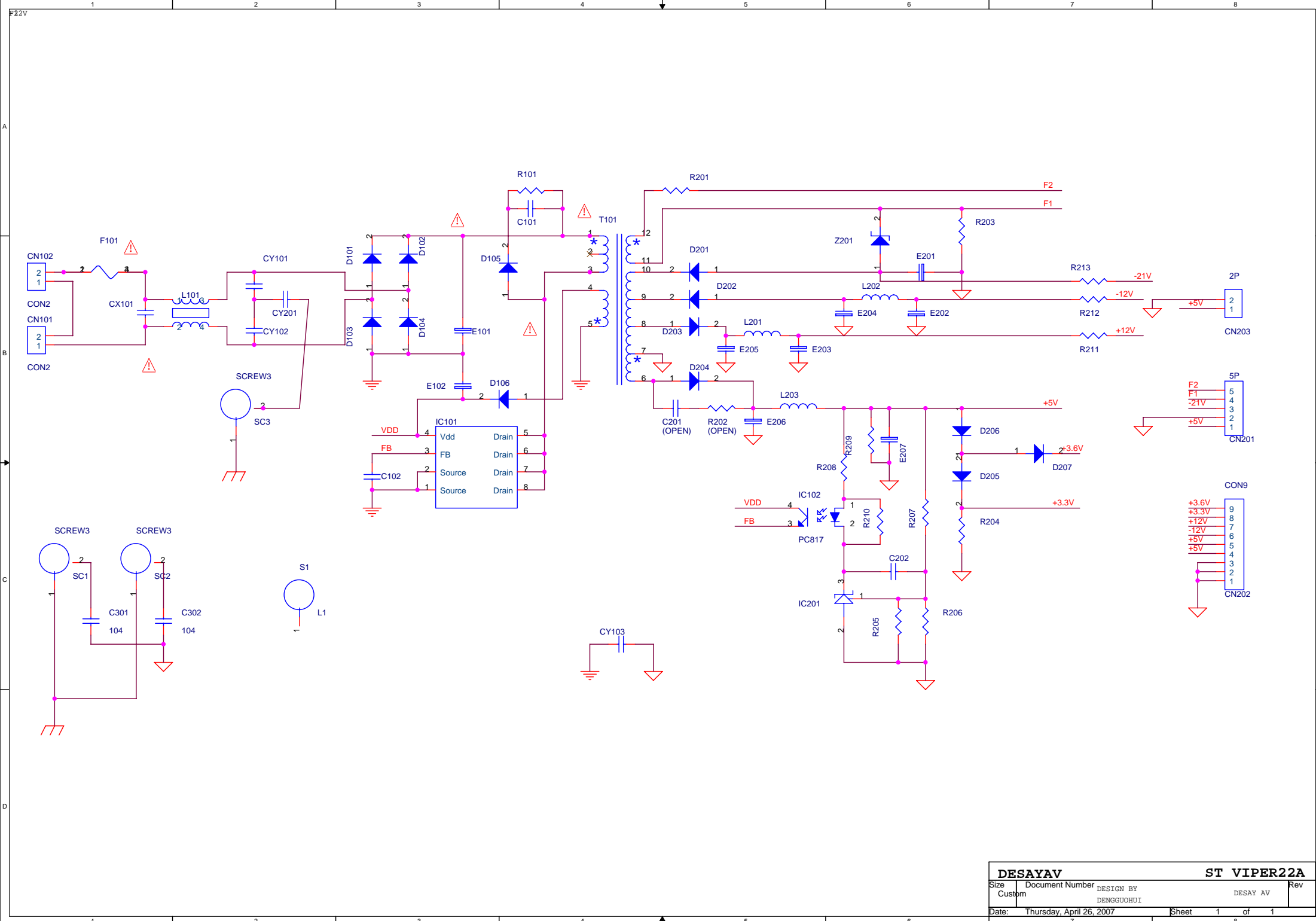
机型: DS-A209

DESAY 德赛视听

设计: 王廷 校对: 比例

审核: 批准: 第1页共1页

结构图 幅面 A1 文件位置: H



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Size	Document Number	DESIGN BY	Rev
Custom		DENGGUOHUI	DESAY AV
Date:	Thursday, April 26, 2007	Sheet	1 of 1
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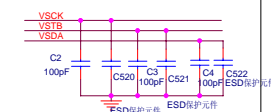
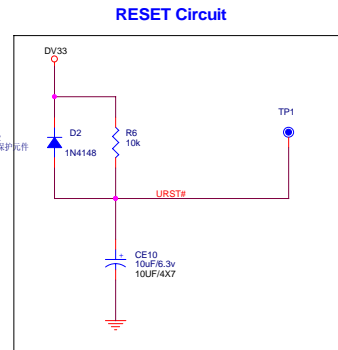
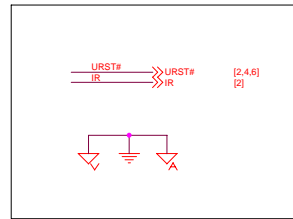
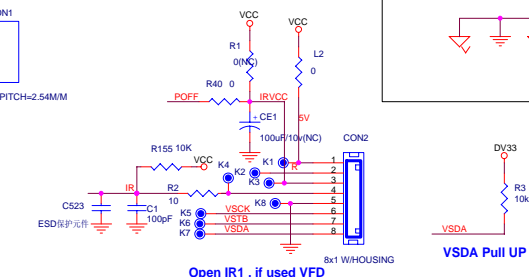
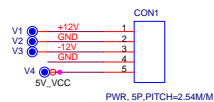
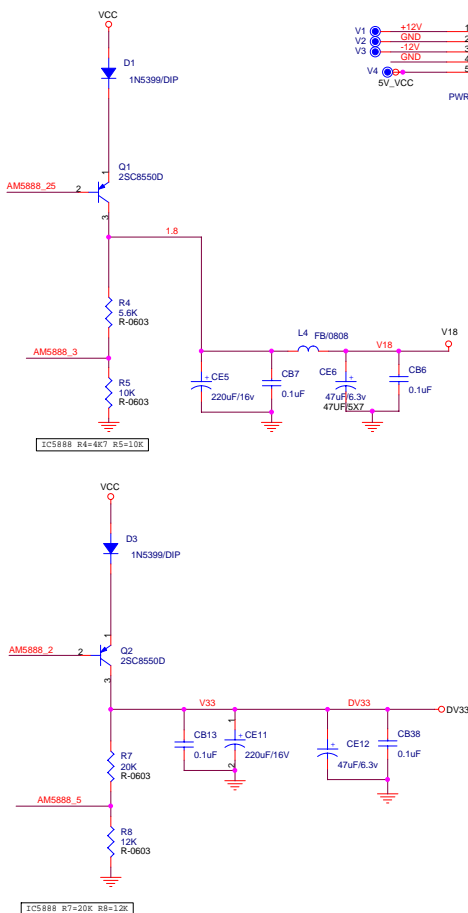
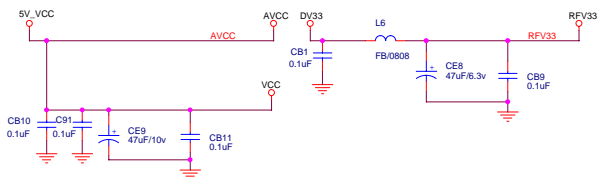
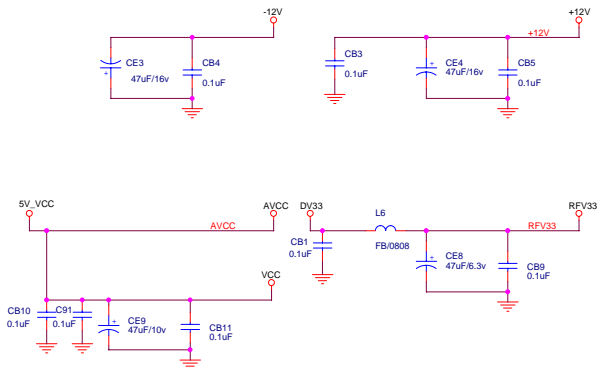
MDxxx

- | | |
|---|----------------------|
| 1 | INDEX & POWER, RESET |
| 2 | MT1389HD |
| 3 | SDRAM & FLASH |
| 4 | VIDEO OUT & AV-CON |
| 5 | AUDIO OUT - WM8766 |
| 6 | MT1392 HDMI |

Rev	History	P#	Date
V1	The original released. Supports 1389HD & Memory Card Interface		2005/03/07
V2	add protection circuit for OPV	2	2005/05/30
V3	delete U20 and U21	4	2005/07/28
V4	add R110 and R111 for HDMI I2C	6	2005/10/27

NAME	TYPE	DEVICE
VCC	Digital 5V	SUPPLY
DV33	Digital 3.3V	MT1389E
RFV33	Servo 3.3V	MT1389E
AV33	Laser Diode 3.3V	
V18	Digital 1.8V	MT1389E
SD33	Digital 3.3V	SDRAM
+12V	Audio +12V	OP AMP.
-12V	Audio -12V	OP AMP.
AVDD5	Audio 5V	Audio DAC
DVDD3	Audio 3.3V	Audio DAC

- [2] V5CK >>> V5CK
- [2] V5DA >>> V5DA
- [2] V5TB >>> V5TB
- [2] P5FF >>> P5FF
- [2] R >>> R

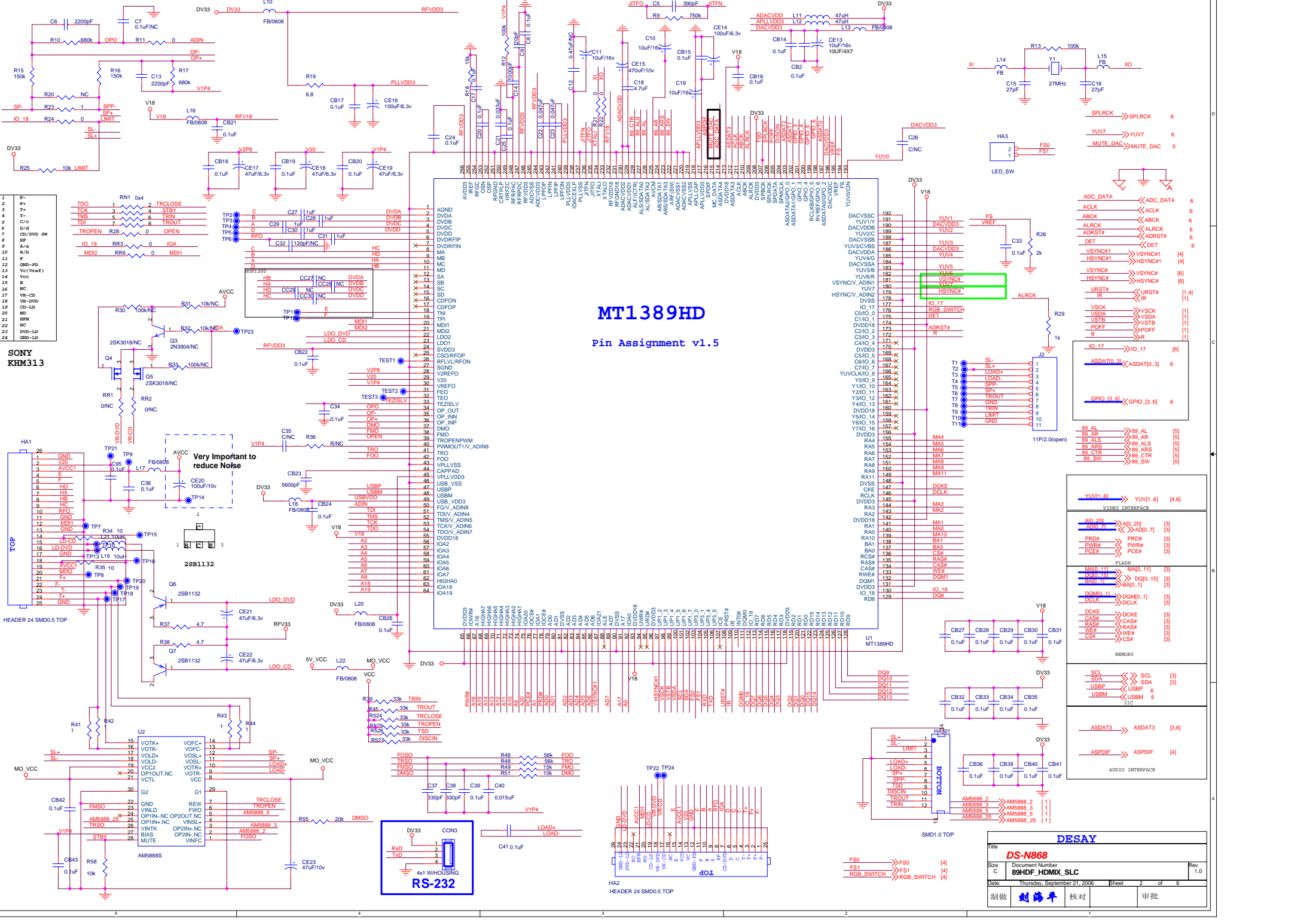


- AMS888_2 >>> AMS888_2 [2]
- AMS888_3 >>> AMS888_3 [2]
- AMS888_5 >>> AMS888_5 [2]
- AMS888_25 >>> AMS888_25 [2]

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制做	刘海平	核对	审批

MT1389HD

Pin Assignment v1.5



SPLRCK	<<>	SPLRCK	6
YUV7	<<>	YUV7	6
MUTE_DAC	<<>	MUTE_DAC	5
ADC_DATA	<<>	ADC_DATA	6
ACLK	<<>	ACLK	6
ABCK	<<>	ABCK	6
ALRCK	<<>	ALRCK	6
ADRST#	<<>	ADRST#	6
DET	<<>	DET	6
VSYNCP1	<<>	VSYNCP1	[4]
HSYNCP1	<<>	HSYNCP1	[4]
VSYNCP#	<<>	VSYNCP#	[6]
HSYNCP#	<<>	HSYNCP#	[6]
URST#	<<>	URST#	[1,4]
IR	<<>	IR	[1]
VSKC	<<>	VSKC	[1]
VSDA	<<>	VSDA	[1]
VSTB	<<>	VSTB	[1]
POFF	<<>	POFF	[1]
R	<<>	R	[1]
IO_17	<<>	IO_17	[6]
ASDAT0_3I	<<>	ASDAT0_3I	6
GPIO_B_6I	<<>	GPIO_B_6I	6
89_AL	<<>	89_AR	[5]
89_ARS	<<>	89_ARS	[5]
89_ALS	<<>	89_ALS	[5]
89_CTR	<<>	89_CTR	[5]
89_SW	<<>	89_SW	[5]

YUV1_6I	<<>	YUV1_6I	[4,6]
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VIDEO INTERFACE

AD_20I	<<>	AD_20I	[3]
AD_0_7I	<<>	AD_0_7I	[3]
PRD#	<<>	PRD#	[3]
PWR#	<<>	PWR#	[3]
PCEN	<<>	PCEN	[3]

FLASH

MA_10I	<<>	MA_10I	[3]
BA_0_1I	<<>	BA_0_1I	[3]
DOM_0_1I	<<>	DOM_0_1I	[3]
DCKE	<<>	DCKE	[3]
DCKE#	<<>	DCKE#	[3]
CAS#	<<>	CAS#	[3]
RAS#	<<>	RAS#	[3]
WE#	<<>	WE#	[3]
CS#	<<>	CS#	[3]

SCL	<<>	SCL	[3]
SDA	<<>	SDA	[3]
USBP	<<>	USBP_6	[3]
USBM	<<>	USBM_6	[3]

MEMORY

ASDAT3	<<>	ASDAT3	[3,6]
ASPDF	<<>	ASPDF	[4]

ASDAT3	<<>	ASDAT3	[3,6]
ASPDF	<<>	ASPDF	[4]

AUDIO INTERFACE

DESAY

DS-N868

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制做: 刘海平 核对: 审批:

1	F-
2	F+
3	T-
4	T+
5	C/c
6	D/d
7	CD/DVD SW
8	RF
9	A/a
10	B/b
11	F
12	QBD-PD
13	Vc(Vcc-E)
14	Vcc
15	E
16	K
17	VB-CD
18	VB-D
19	CD-LD
20	HD
21	RFM
22	HC
23	DVD-LD
24	GND-LD

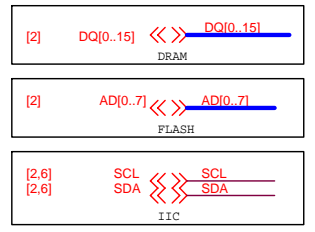
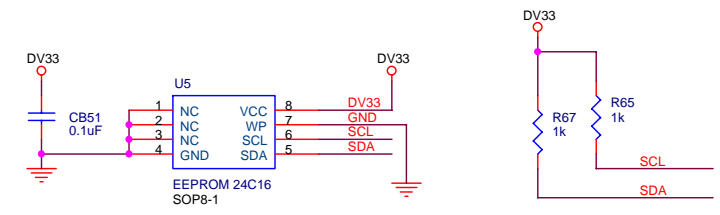
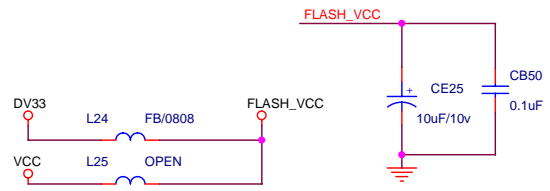
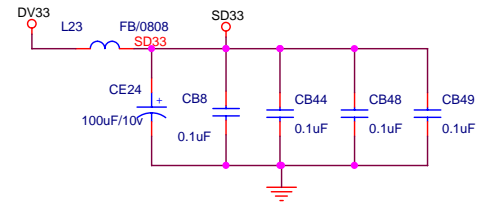
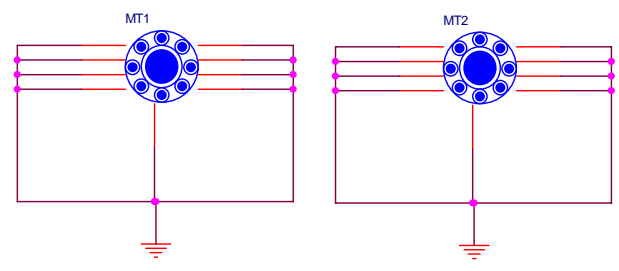
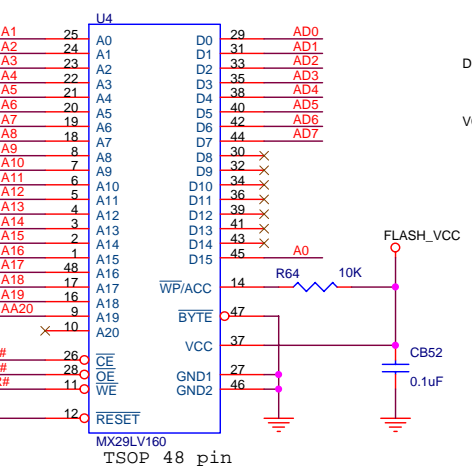
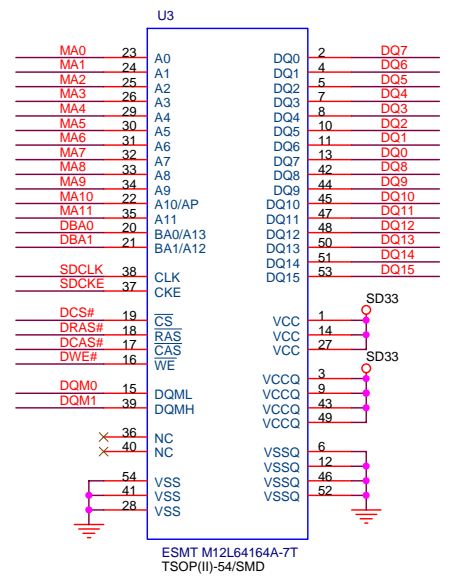
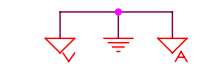
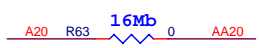
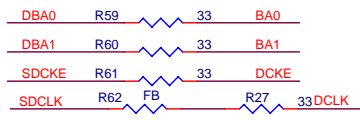
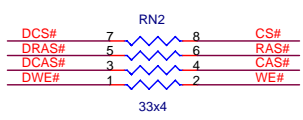
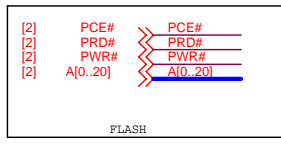
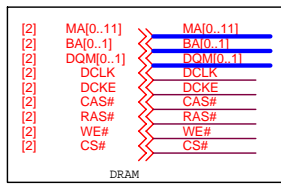
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2	TRCLOSE
3	STBY
4	TRM
5	TROUT
6	OPEN
7	IOA
8	MDI1

1	GND
2	V20
3	AVCC1
4	F
5	HA
6	HD
7	HA
8	HB
9	HC
10	RFO
11	GND
12	NDI
13	GND
14	LD-CV
15	LD-DVD
16	GND
17	LD-DVD
18	GND
19	AVCC1
20	MDI2
21	F
22	T-
23	T+
24	GND
25	GND

1	VOTK+
2	VOTC
3	VOLD+
4	VOLS+
5	VOCZ
6	VOTR+
7	VOTR-
8	VCTL
9	VCC
10	GND
11	VIND
12	OPIN-NC
13	OPIN-NC
14	VINSL+
15	VINSL-
16	VINTK
17	BIAS
18	MUTE
19	VINFC
20	GND
21	FWD
22	AM5888_5
23	OPIN-NC
24	OPIN-NC
25	AM5888_3
26	AM5888_2
27	AM5888_1
28	FOSO

1	RxD
2	TxD
3	GND

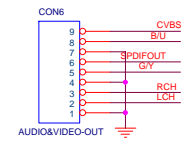
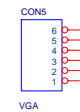
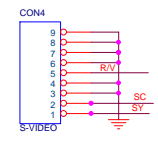
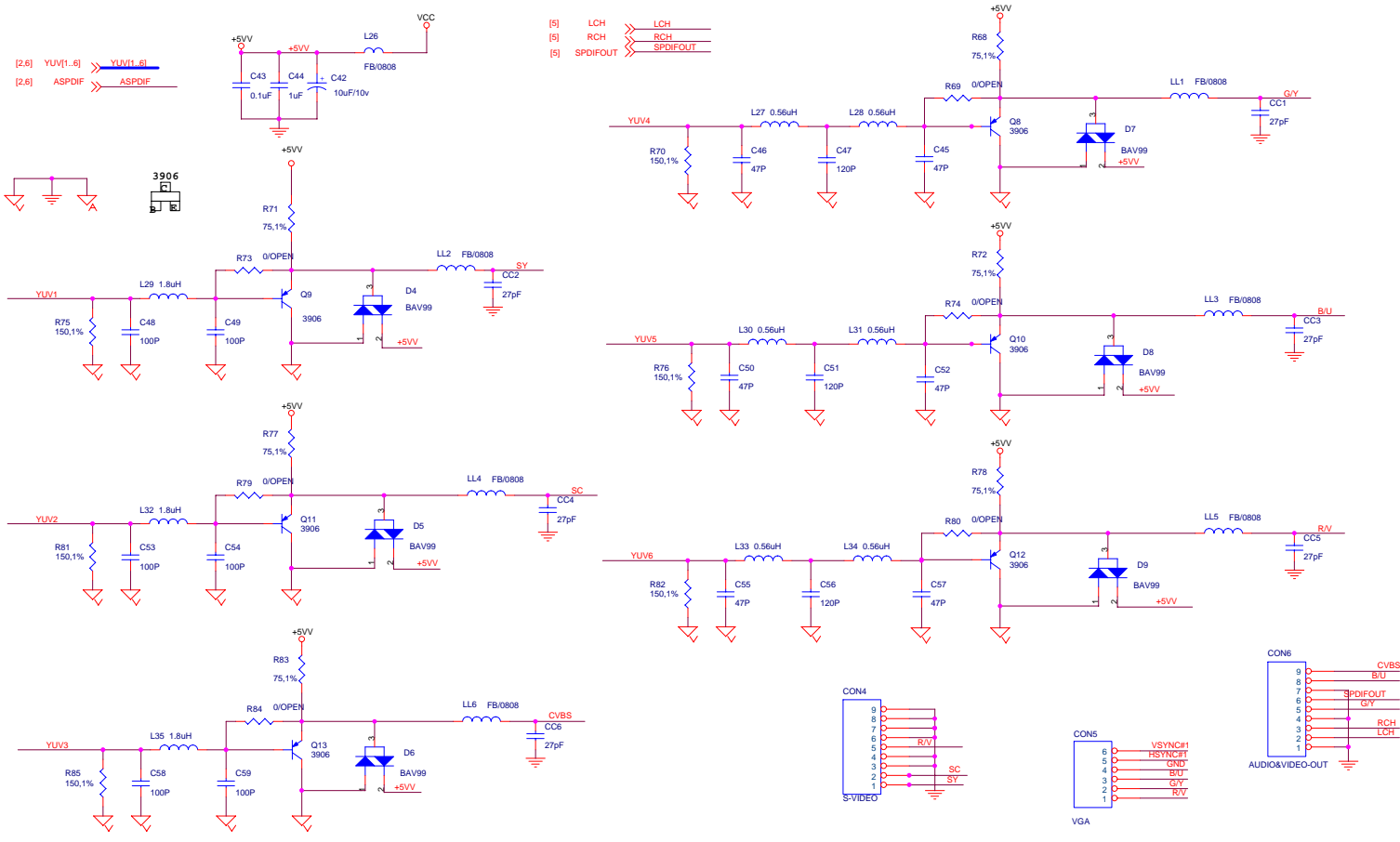
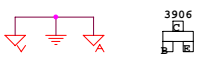
RS-232



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Size: B	Document Number: SDRAM&FLASH	Rev: 1.0	
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制做: 刘海平	核对:		审批:

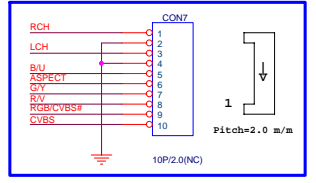
[2,6] YUV[1..6] >> YUV[1..6]
 [2,6] ASPDIF >> ASPDIF

[5] LCH >> LCH
 [5] RCH >> RCH
 [5] SPDIFOUT >> SPDIFOUT



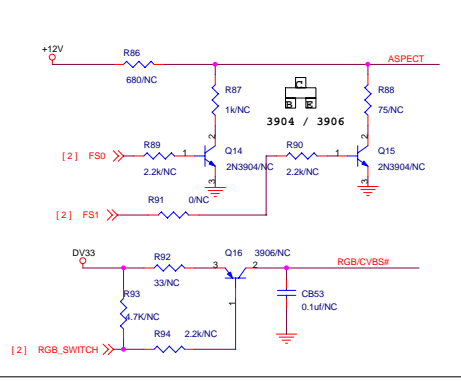
VSYNC#1 >> VSYNC#1 [2]
 HSYNC#1 >> HSYNC#1 [2]

SCART CONNECTOR

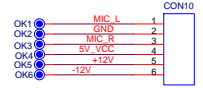
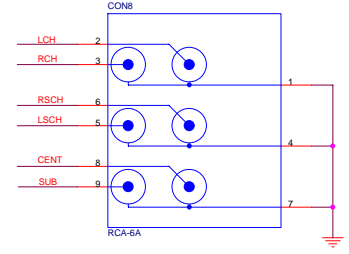
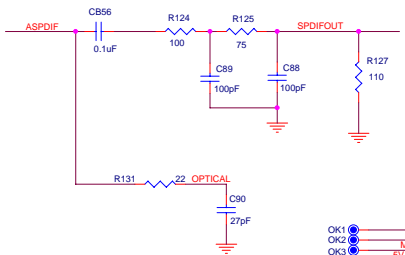
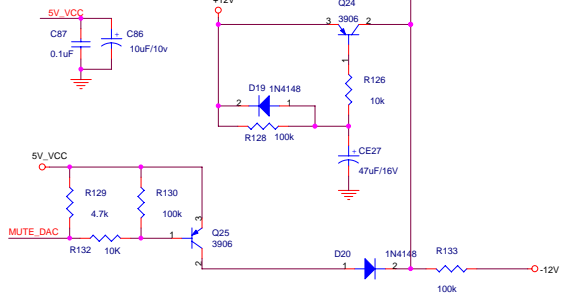
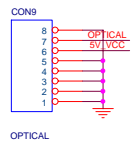
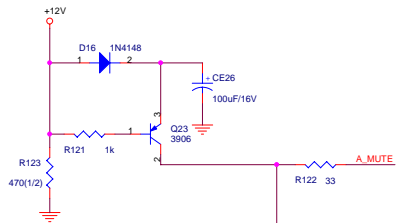
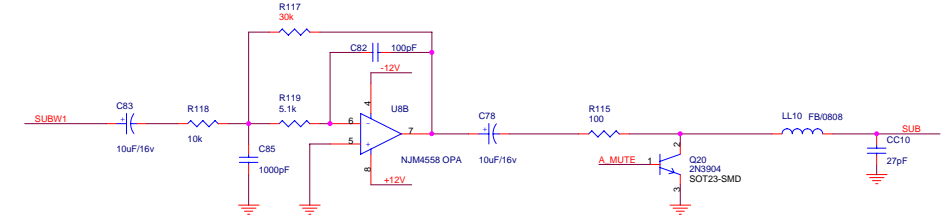
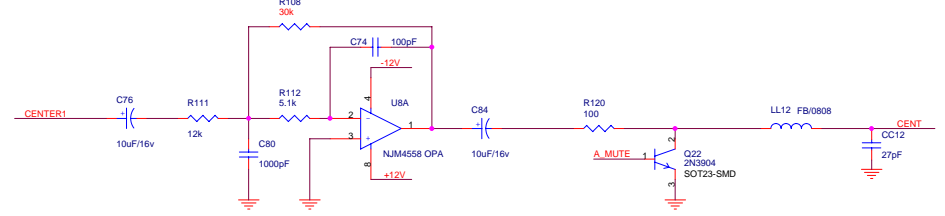
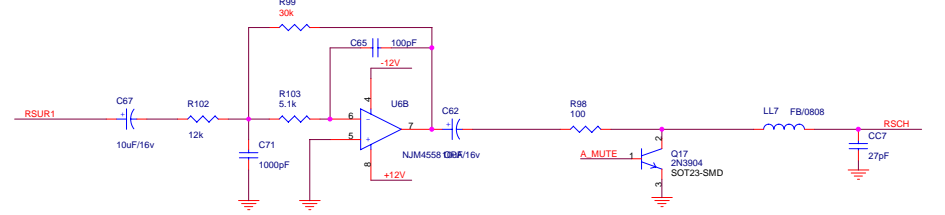
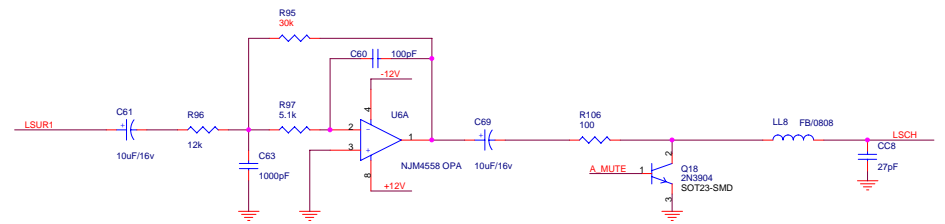
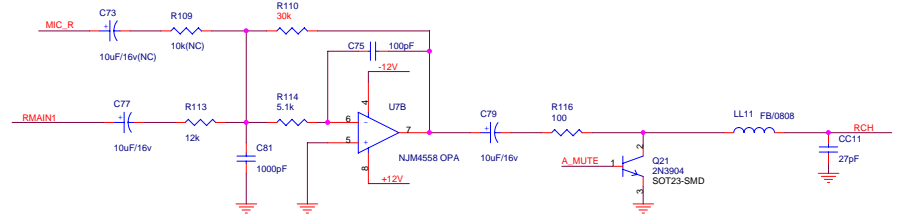
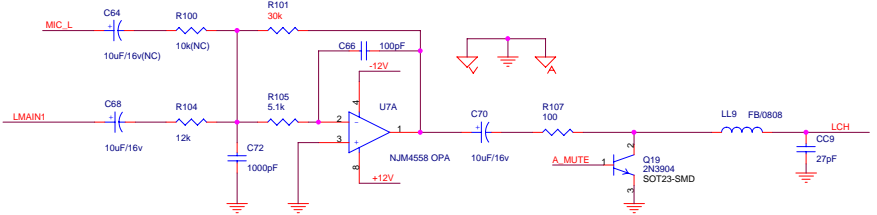
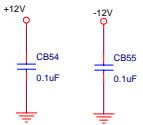
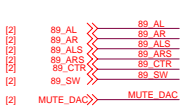


FS0 >> FS0 [2]
 FS1 >> FS1 [2]
 RGB_SWITCH >> RGB_SWITCH [2]

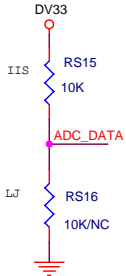
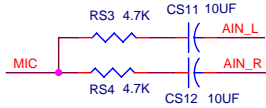
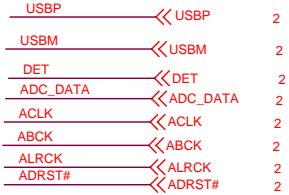
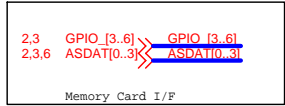
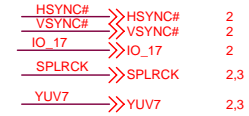
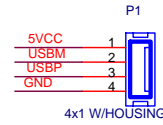
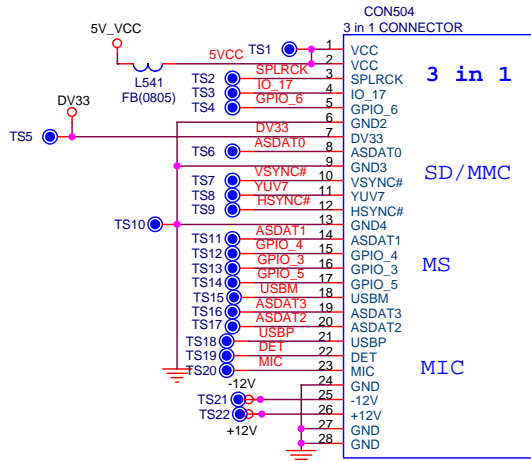
SCART CONTROL



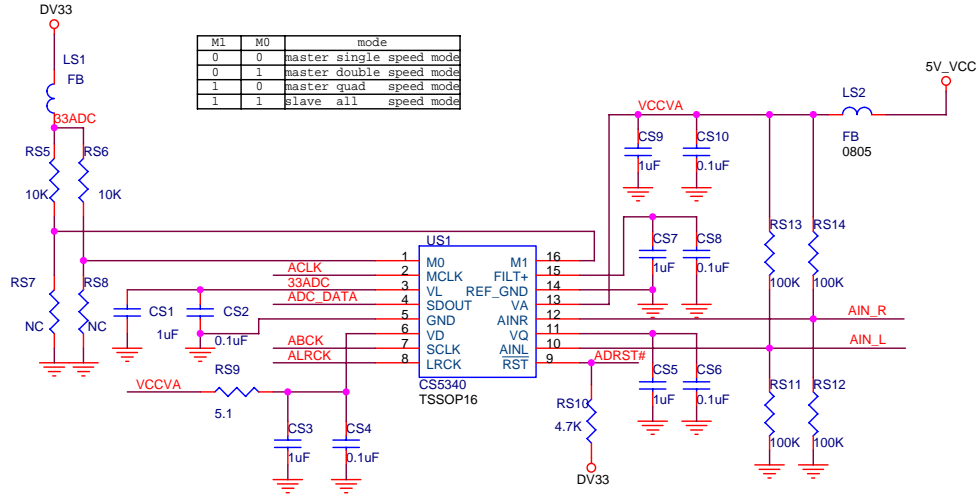
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C	VIDEO OUT & AV-CON	1.0	
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制做	刘海平	核对	审批



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DS-N868			
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制做	刘海平 核对	审批	



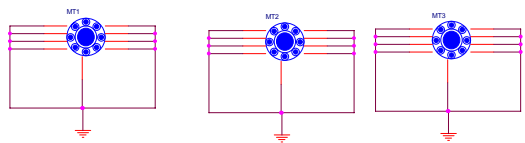
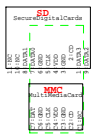
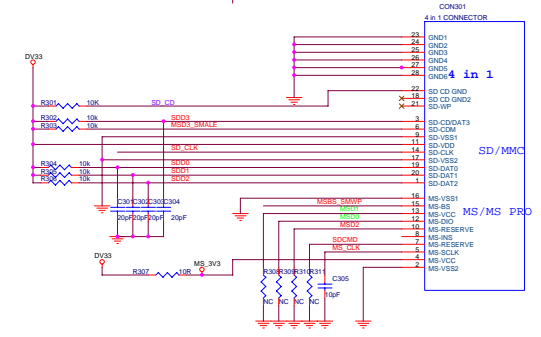
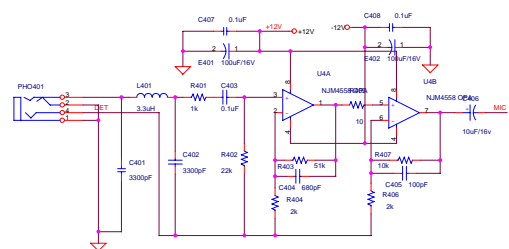
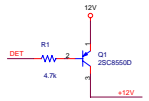
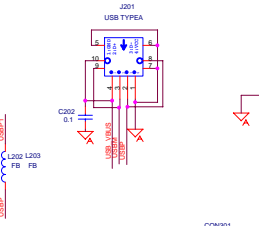
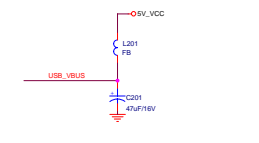
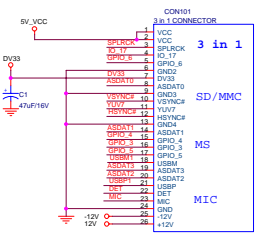
M1	M0	mode
0	0	master single speed mode
0	1	master double speed mode
1	0	master quad speed mode
1	1	slave all speed mode



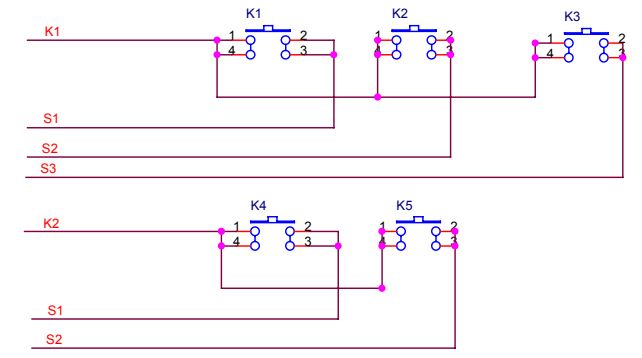
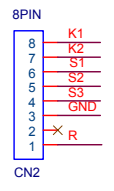
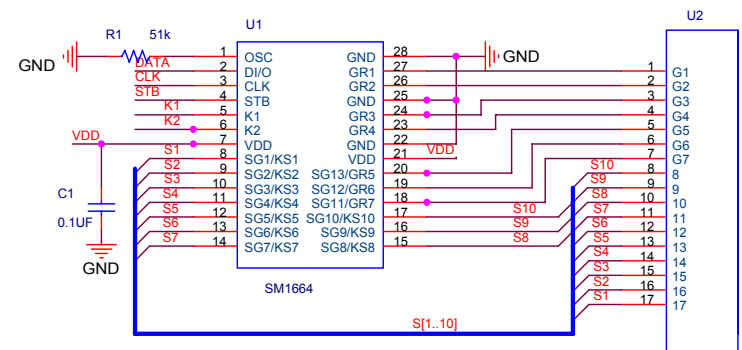
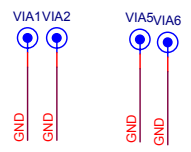
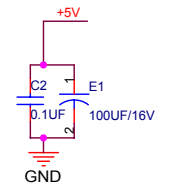
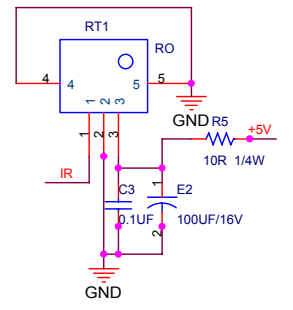
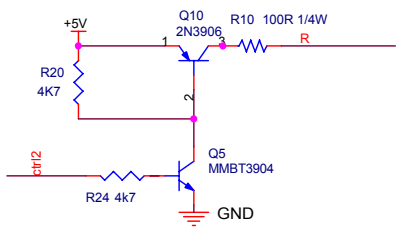
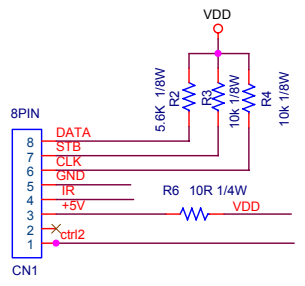
Slave IIS 24bit

DESAY			
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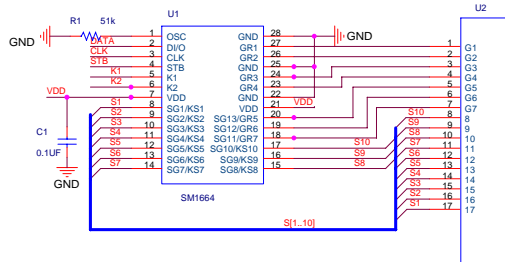
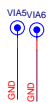
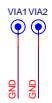
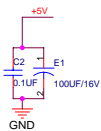
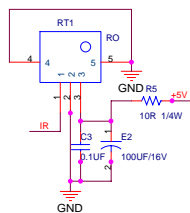
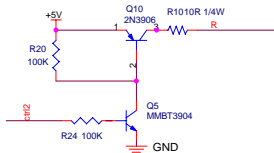
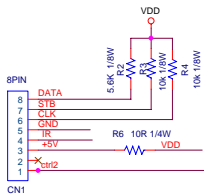
- 增加部分
- SPLBCK SD_CD
 - ASDAT0 SD_CLK
 - ASDAT1 CFA0_MBSB_BMWP
 - ASDAT2 MS_CLK
 - ASDAT3 CFI00_SDCMD
 - GPD_3 CFI00_MSD0
 - GPD_4 CFI00_MSD1
 - GPD_5 CFA1_MSD2
 - GPD_6 CFA2_MSD3_SMALE
 - YU7 CFE1_SDD1
 - HSYNC4 CFI0_SDD2
 - VSYNCE CFI1_SDD3
 - IO_17 CFI0RDV_SDD3
 - MSD0 CFI0_SDD0
 - CFDB CFI0_MSD1
 - MSD1 CFI0_MSD1
 - CFD6 CFI0_MSD1
 - ASDAT0 SD_CLK
 - ASDAT1 MBSB_BMWP
 - ASDAT2 MS_CLK
 - ASDAT3 MSD3_SMALE
 - GPD_3 MSD1
 - GPD_4 MSD1
 - GPD_5 MSD1
 - GPD_6 SDCMD
 - YU7 SDD1
 - HSYNC4 SDD2
 - VSYNCE SDD3
 - IO_17 SDD3



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