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SIMetrix/SIMPLIS Intro

SIMetrix/SIMPLIS Intro is a free version of the program with no license or copying restrictions. Virtually all features are enabled but a circuit size limit applies. The limits for the Intro versions are generous enough for them to be used for real work and we are happy for users to do so.

System Requirements

SIMetrix/SIMPLIS Intro require Windows 8 (standard, Professional or Enterprise), Windows 7 (Home Premium or better), Windows Vista (Home Basic or better) or Windows XP (Home or Professional, with latest service packs). Both 32 and 64 bit platforms are supported.

SIMetrix/SIMPLIS Intro Limitations

Features

All features of the full production version are enabled except:

1. The command line is not present. This effectively inhibits any features that require the command line. There are, however, very few of these
2. User scripts
3. The Verilog-A compiler is not available
4. Verilog-HDL mixed signal simulation is not available
5. Custom menu and key definitions
6. The PSpice translator is limited to small schematics
7. SPICE3 raw file import
8. Safe Operating Area testing is not available
9. The menu/key editor is not available

Some of the above features can be enabled for 30 days using the unlock feature. See menu **Help | Unlock Features...**

SIMetrix Circuit Size Limits

There is a limit to the size of circuit that can be simulated. The exact limits for the SIMetrix simulator are:

140 analog nodes (internal and external - see note below)
360 digital nodes
720 digital ports
300 digital components
360 digital outputs

The node count includes all circuit nodes and some of these may be internal to device models. Opamp models, for example, will always have a number of internal nodes depending its complexity.

SIMPLIS Circuit Size Limits

The exact limits for the SIMPLIS simulator are:

1. A total of no more than 15 state variables. A capacitor or an inductor each requires one state variable. Each time-varying or small-signal AC source requires one state variable, with the exception of SINusoidal or COSinusoidal sources, which require two state variables per source.
2. A total of no more than 10 capacitors or inductors combined.
3. A total of no more than six switches, simple or transistor.
4. A total of no more than six logic gates.
5. A total of no more than 26 "states." Each PWL element requires one state. Each switch requires one state. Each time-varying source requires one state. Each logic gate requires one state.

6. A total of no more than 100 new topologies. 100 topologies will be enough for simple switching circuits that use simple models only. More complex circuits or circuits that have more complicated models may exceed this limit

There are no Limits on...

...the complexity of arbitrary source expressions or arbitrary logic block definitions.

There are also no limits on the size of schematic that can be drawn or printed. You are welcome to use SIMetrix as a free general purpose schematic drawing tool.

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