

7.6 Initial code for reference**7.6.1 The reference setting of Normal Display for Register-Content Interface Mode****7.6.1.1 The reference setting of CMO 3.0" Panel****void HX8352_Init_CMO30(void)**

```

{
    RESET();
    DelayX1ms(150);

    Set_LCD_8B_REG(0x83,0x02);    // TESTM=1
    Set_LCD_8B_REG(0x85,0x02);    // VDC_SEL=010.
    Set_LCD_8B_REG(0x8B,0x00);    // STBA[15:8]=0x00
    Set_LCD_8B_REG(0x8C,0xB3);    // STBA[7]=1, STBA[5:4]=11, STBA[1:0]=11
    Set_LCD_8B_REG(0x83,0x00);    // TESTM=0

// Gamma Setting
    Set_LCD_8B_REG(0x3E,0xA0);
    Set_LCD_8B_REG(0x3F,0x03);
    Set_LCD_8B_REG(0x40,0x00);
    Set_LCD_8B_REG(0x41,0x45);
    Set_LCD_8B_REG(0x42,0x03);
    Set_LCD_8B_REG(0x43,0x47);
    Set_LCD_8B_REG(0x44,0x23);
    Set_LCD_8B_REG(0x45,0x77);
    Set_LCD_8B_REG(0x46,0x01);
    Set_LCD_8B_REG(0x47,0x1F);
    Set_LCD_8B_REG(0x48,0x0F);
    Set_LCD_8B_REG(0x49,0x03);

// Power Supply Setting
    Set_LCD_8B_REG(0x17,0xC1);    // RADJ=1100, OSC_EN=1
    Set_LCD_8B_REG(0x23,0x01);    // TE 0n
    Set_LCD_8B_REG(0x2B,0xF9);    // N_DCDC=0xF9.

    DelayX1ms(10);

    Set_LCD_8B_REG(0x1B,0x14);    // BT=0001, AP=100
    Set_LCD_8B_REG(0x1A,0x11);    // VC3=001, VC1=001 (VLCD/DDVDH)=6.45V)
    Set_LCD_8B_REG(0x1C,0x0E);    // VRH=1110 (VREG1=6.0V)
    Set_LCD_8B_REG(0x1D,0x0F);    // VBGP=1111
    Set_LCD_8B_REG(0x1F,0x3B);    // VCM=011_1011
    DelayX1ms(20);

    Set_LCD_8B_REG(0x19,0x0A);    // GASENB=0, PON=0, DK=1, XDK=0, VLCD_TRI=1, STB=0
    Set_LCD_8B_REG(0x19,0x1A);    // GASENB=0, PON=1, DK=1, XDK=0, VLCD_TRI=1, STB=0
    DelayX1ms(40);

    Set_LCD_8B_REG(0x19,0x12);    // GASENB=0, PON=1, DK=0, XDK=0, VLCD_TRI=1, STB=0,
    // VLCD=2XVCI by 2 CAPs

    DelayX1ms(40);

    Set_LCD_8B_REG(0x1E,0x2E);    // VCOMG=1, VDV=0_1110
    //VCOMG NEW LOCATION
    Set_LCD_8B_REG(0x83,0x02);    // TESTM=1
    Set_LCD_8B_REG(0x93,0x10);    // R93[4]=1, VCOMG=1
    Set_LCD_8B_REG(0x83,0x00);    // TESTM=0
    DelayX1ms(100);
}

```

// DGC Function Enable

```
Set_LCD_8B_REG(0x5A,0x01);
DGC_PA_REG(0x5C);
```

// Display ON Setting

```
Set_LCD_8B_REG(0x3C,0xFF);           // N_SAP=0111 1111
Set_LCD_8B_REG(0x3D,0x0E);           // I_SAP=0000 1110
Set_LCD_8B_REG(0x34,0x38);           // EQS=1000 0111
Set_LCD_8B_REG(0x35,0x38);           // EQP=0011 1000
Set_LCD_8B_REG(0x24,0x38);           // GON=1, DTE=1, D=10
DelayX1ms(40);
Set_LCD_8B_REG(0x24,0x3C);           // GON=1, DTE=1, D=11

Set_LCD_8B_REG(0x16,0x08);           // BGR=1

Set_LCD_8B_REG(0x01,0x06);           // INVON=1, NORNO=1
}
```

void DGC_PA_REG(unsigned char ADDR)

```
{
    unsigned char i;

    M51_CTRL_LCD_nCS = 0;
    M51_CTRL_LCD_RS = 0;
    WR_8B_FORMAT(ADDR);               //ADDR=0x5C.
    M51_CTRL_LCD_RS = 1;

    for( i=0; i<=2; i++)
    {
        WR_8B_FORMAT(0x00);
        WR_8B_FORMAT(0x04);
        WR_8B_FORMAT(0x08);
        WR_8B_FORMAT(0x0C);
        WR_8B_FORMAT(0x10);
        WR_8B_FORMAT(0x14);
        WR_8B_FORMAT(0x18);
        WR_8B_FORMAT(0x1C);
        WR_8B_FORMAT(0x20);
        WR_8B_FORMAT(0x24);
        WR_8B_FORMAT(0x28);
        WR_8B_FORMAT(0x2C);
        WR_8B_FORMAT(0x30);
        WR_8B_FORMAT(0x38);
        WR_8B_FORMAT(0x3C);
        WR_8B_FORMAT(0x3F);
        WR_8B_FORMAT(0x43);
        WR_8B_FORMAT(0x46);
        WR_8B_FORMAT(0x48);
        WR_8B_FORMAT(0x4C);
        WR_8B_FORMAT(0x50);
        WR_8B_FORMAT(0x54);
        WR_8B_FORMAT(0x58);
        WR_8B_FORMAT(0x5C);
        WR_8B_FORMAT(0x60);
        WR_8B_FORMAT(0x64);
        WR_8B_FORMAT(0x66);
        WR_8B_FORMAT(0x6A);
    }
}
```

```
WR_8B_FORMAT(0x6E);  
WR_8B_FORMAT(0x71);  
WR_8B_FORMAT(0x75);  
WR_8B_FORMAT(0x79);  
WR_8B_FORMAT(0x7D);  
WR_8B_FORMAT(0x81);  
WR_8B_FORMAT(0x85);  
WR_8B_FORMAT(0x89);  
WR_8B_FORMAT(0x8E);  
WR_8B_FORMAT(0x92);  
WR_8B_FORMAT(0x98);  
WR_8B_FORMAT(0x9C);  
WR_8B_FORMAT(0xA0);  
WR_8B_FORMAT(0xA4);  
WR_8B_FORMAT(0xA9);  
WR_8B_FORMAT(0xAE);  
WR_8B_FORMAT(0xB1);  
WR_8B_FORMAT(0xB5);  
WR_8B_FORMAT(0xB9);  
WR_8B_FORMAT(0xBD);  
WR_8B_FORMAT(0xC1);  
WR_8B_FORMAT(0xC5);  
WR_8B_FORMAT(0xCA);  
WR_8B_FORMAT(0xCF);  
WR_8B_FORMAT(0xD4);  
WR_8B_FORMAT(0xDA);  
WR_8B_FORMAT(0xDE);  
WR_8B_FORMAT(0xE1);  
WR_8B_FORMAT(0xE5);  
WR_8B_FORMAT(0xE9);  
WR_8B_FORMAT(0xED);  
WR_8B_FORMAT(0xEF);  
WR_8B_FORMAT(0xF2);  
WR_8B_FORMAT(0xF5);  
WR_8B_FORMAT(0xF8);  
WR_8B_FORMAT(0xFF);
```

```
}
```

```
M51_CTRL_LCD_nCS = 1;
```

```
}
```