



MICROCHIP PIC18F2331/2431/4331/4431

28/40-Pin Enhanced FLASH Motor Control Microcontroller Product Brief

High Performance RISC CPU:

- C compiler optimized architecture/instruction set
- Linear program memory addressing to 16 Kbytes
- Linear data memory addressing to 768 bytes
- Up to 256 bytes of Data EEPROM
- Up to 10 MIPS operation:
 - DC - 40 MHz osc./clock input
 - 4 MHz - 10 MHz osc./clock input with PLL active
- 16-bit wide instructions, 8-bit wide data path
- Priority levels for interrupts
- 31-level, software accessible hardware stack
- 8 x 8 Single Cycle Hardware Multiplier

Peripheral Features:

- High current sink/source 25 mA/25 mA
- Four external interrupt pins (40-pin devices only)
- Timer0 module: 8-bit/16-bit timer/counter
- Timer1 module: 16-bit timer/counter
- Timer2 module: 8-bit timer/counter
- General purpose timer module: 16-bit timer/counter
- Motor PWM module
 - 3 complementary PWM outputs
 - Supports edge- or center-aligned operation
 - Flexible deadband generator (resolution: 100 ns @ 10 MIPS)
 - Hardware fault protection
- Two Capture/Compare/PWM (CCP) modules:
 - Capture is 16-bit, max. resolution 6.25 ns (T_{cy}/16)
 - Compare is 16-bit, max. resolution 100 ns (T_{cy})
 - PWM output: PWM resolution is 1- to 10-bit
- Master Synchronous Serial Port (MSSP) module
Two modes of operation:
 - 3-wire SPI™ (supports all 4 SPI modes)
 - I²C™ Master and Slave mode
- Addressable USART module:
 - Supports RS-485 and RS-232

Analog Features:

- 10-bit Analog-to-Digital Converter (A/D) with up to 9 channels:
 - Simultaneous sampling on 2 channels
 - 200 ksps conversion rate
 - Conversion available during SLEEP
- Programmable 16-level Low Voltage Detection (LVD) module
 - Supports interrupt-on-Low Voltage Detection
- Programmable Brown-out Reset (BOR)

Special Microcontroller Features:

- 100,000 erase/write cycle Enhanced FLASH program memory typical
- 1,000,000 erase/write cycle Data EEPROM memory typical
- 1 second programming time
- Data EEPROM Retention: > 40 years
- Self-reprogrammable under software control
- Power-on Reset (POR), Power-up Timer (PWRT) and Oscillator Start-up Timer (OST)
- Watchdog Timer (WDT) with its own on-chip RC oscillator for reliable operation
- Programmable code protection
- Power saving SLEEP mode
- Selectable oscillator options including:
 - 4X Phase Lock Loop
 - Secondary oscillator (32 kHz) clock input
- In-Circuit Serial Programming™ (ICSP™) via two pins
- MPLAB® In-Circuit Debug (ICD) via two pins

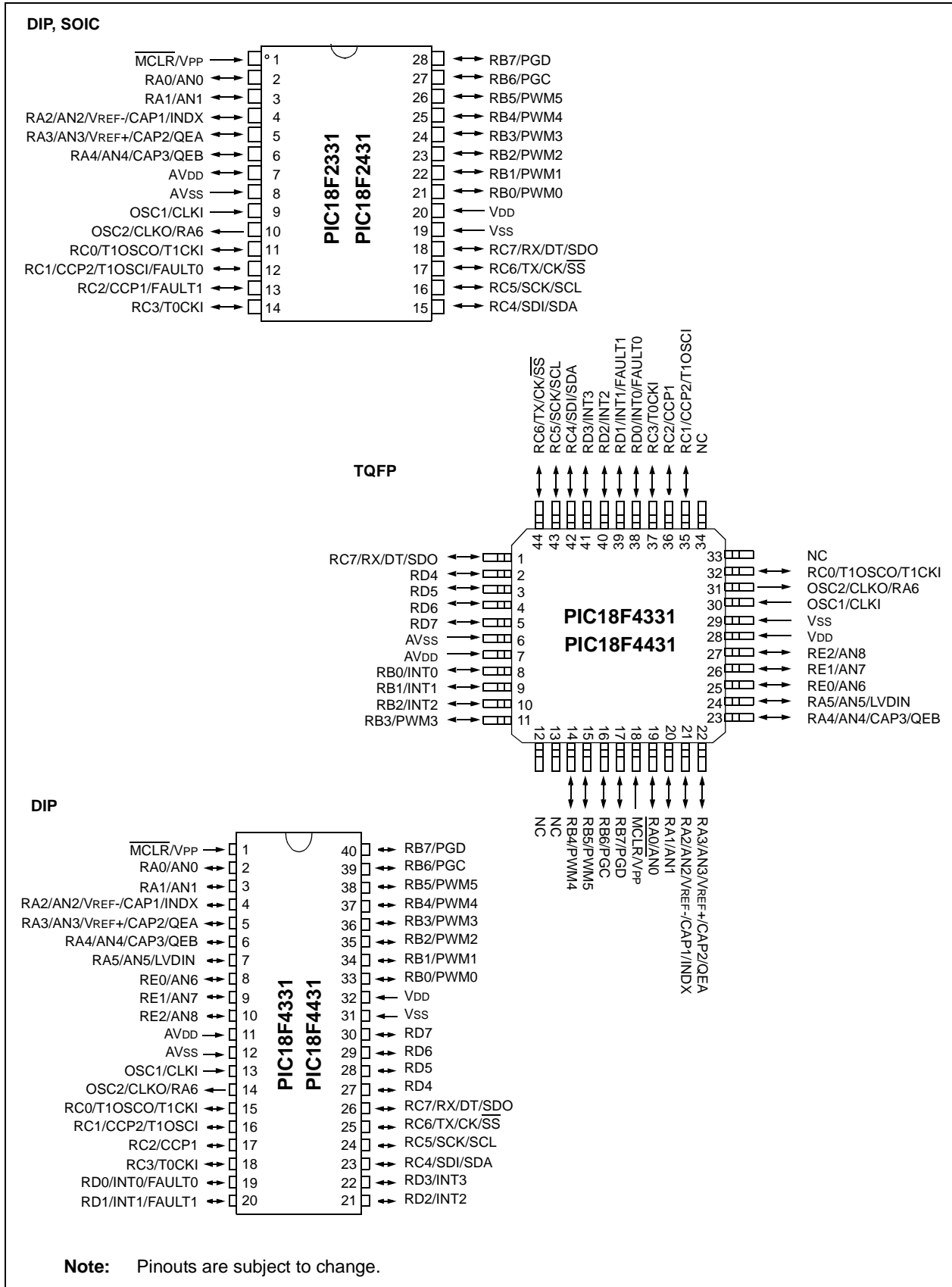
CMOS Technology:

- Low power, high speed FLASH technology
- Fully static design
- Wide operating voltage range (2.0V to 5.5V)
- Industrial temperature range

Device	Program Memory		Data Memory		I/O	10-bit A/D (ch)	CCP (PWM)	MSSP		USART	Timers 8-bit/16-bit	12-bit PWM (ch)	Quadrature Encoder
	Bytes	# Single Word Instructions	SRAM (bytes)	EEPROM (bytes)				SPI	Master I ² C				
PIC18F2331	8K	4096	512	128	22	5	2	Y	Y	Y	1/3	6	Yes
PIC18F2431	16K	8192	768	256	22	5	2	Y	Y	Y	1/3	6	Yes
PIC18F4331	8K	4096	512	128	34	9	2	Y	Y	Y	1/3	6	Yes
PIC18F4431	16K	8192	768	256	34	9	2	Y	Y	Y	1/3	6	Yes

PIC18F2331/2431/4331/4431

Pin Diagrams



Note the following details of the code protection feature on PICmicro® MCUs.

- The PICmicro family meets the specifications contained in the Microchip Data Sheet.
- Microchip believes that its family of PICmicro microcontrollers is one of the most secure products of its kind on the market today, when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the PICmicro microcontroller in a manner outside the operating specifications contained in the data sheet. The person doing so may be engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
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- Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our product.

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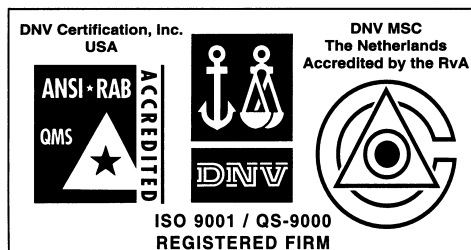
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