

Xilinx - ISE - C:\Users\STRIZ\Desktop\Sis Dig\project2\project2.ise - [proj2.sch]

File Edit View Project Source Process Add Tools Window Help

Sources

Sources for: Behavioral Simulation

- project2
 - xa2s50e-6tq144
 - testmod (testmod.tbw)

Processes

Processes for: testmod

- Add Existing Source
- Create New Source
- View Generated Test Bench As HDL
- Add Test Bench To Project
- Xilinx ISE Simulator
 - Simulate Behavioral Model

run 1000 ns
run 1000 ns
run 1000 ns

Console Errors Warnings Tcl Shell Find in Files Sim Console - testmod

Design Summary proj2.sch testmod.tbw Simulation*

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